

P11 Pro L3 □□□□

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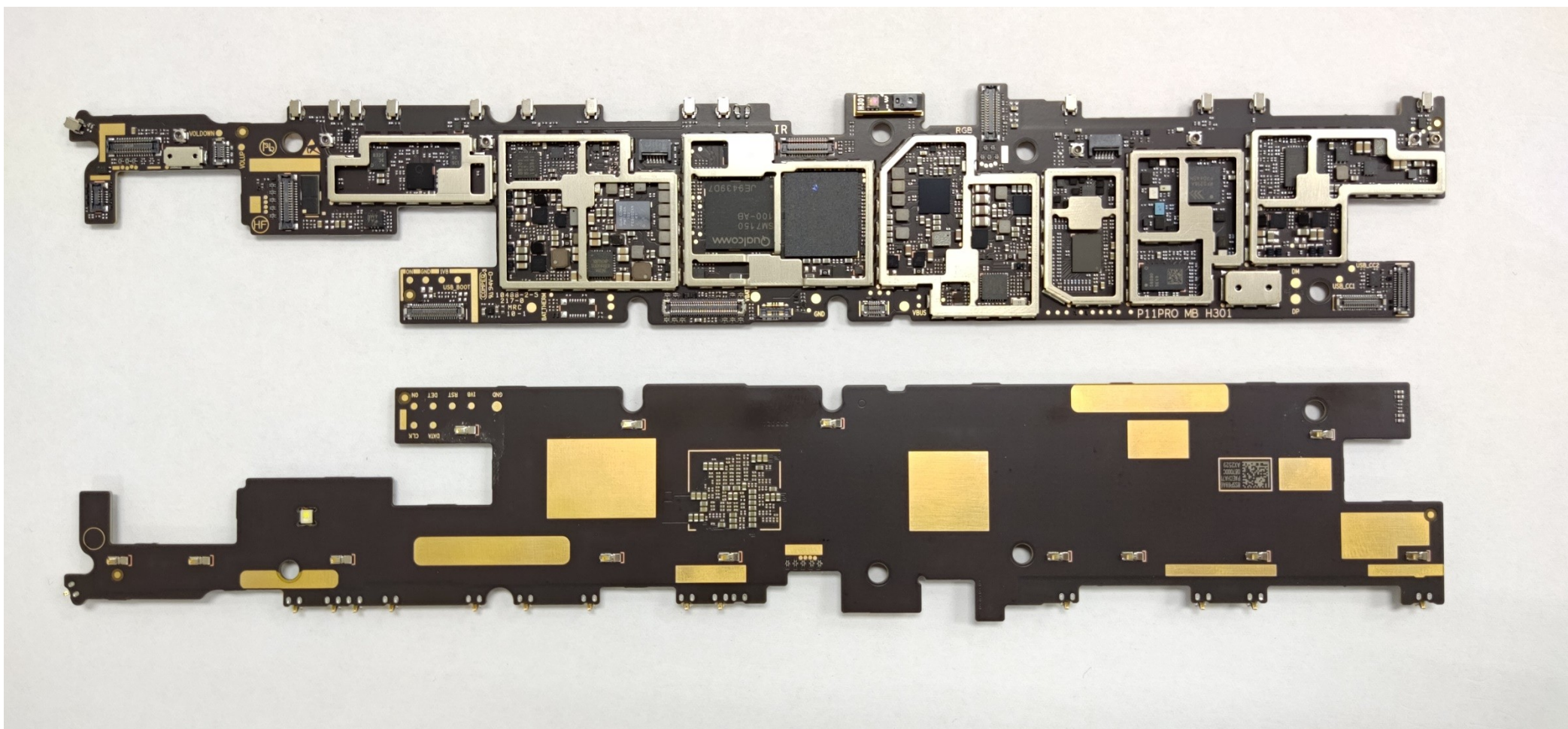
- 1. 〇〇〇〇
- 2. 〇〇〇〇〇〇
- 3. 〇〇〇〇〇〇
- 4. SD 〇〇〇〇〇〇
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- 10. SIM 〇〇〇〇〇〇
- 11. 〇〇〇〇〇〇〇

1. □□□□

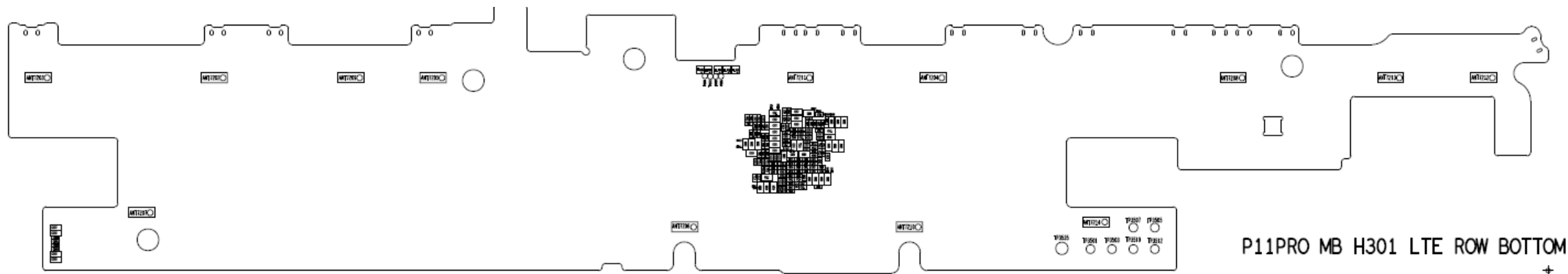
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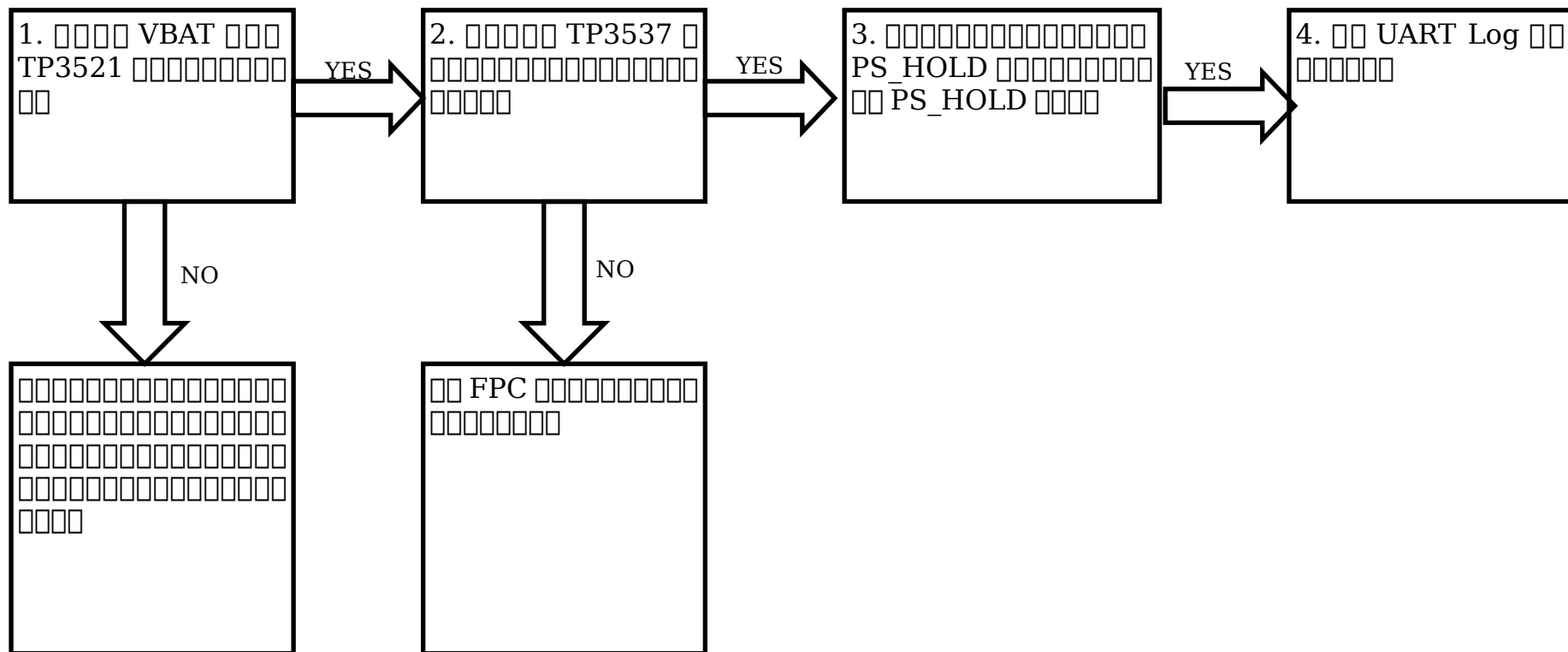




2. □□□□□□

□□□□□□

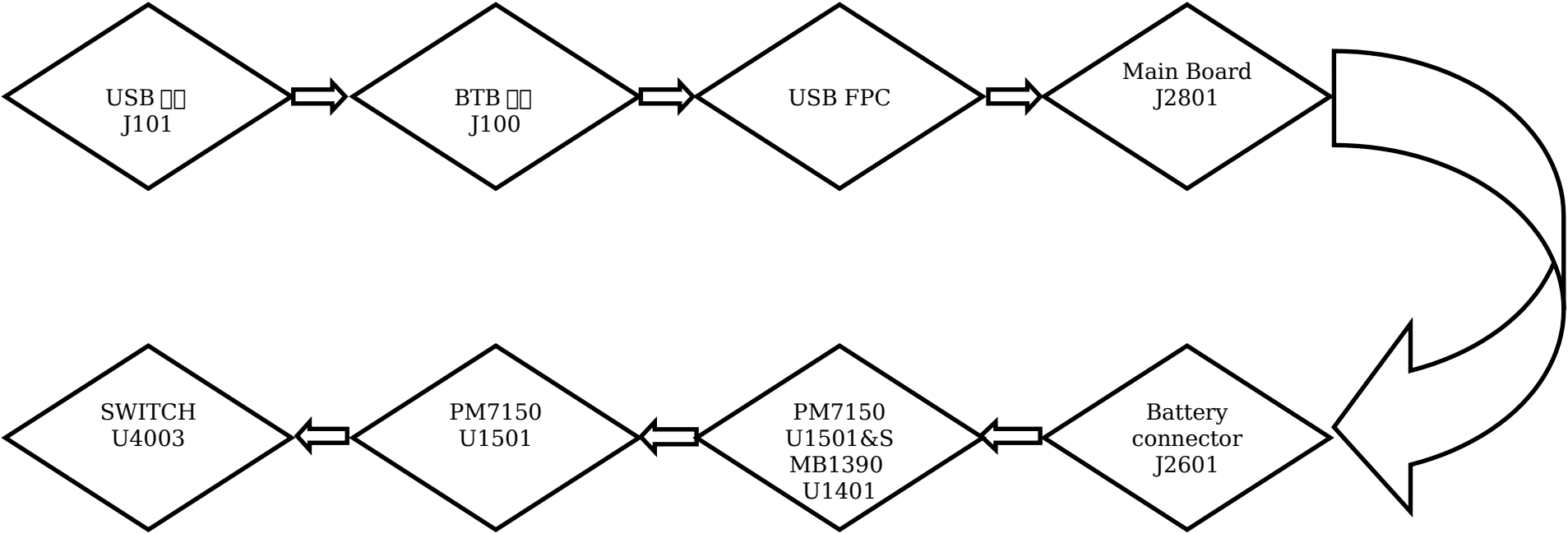
Steps



3. □□□□□□

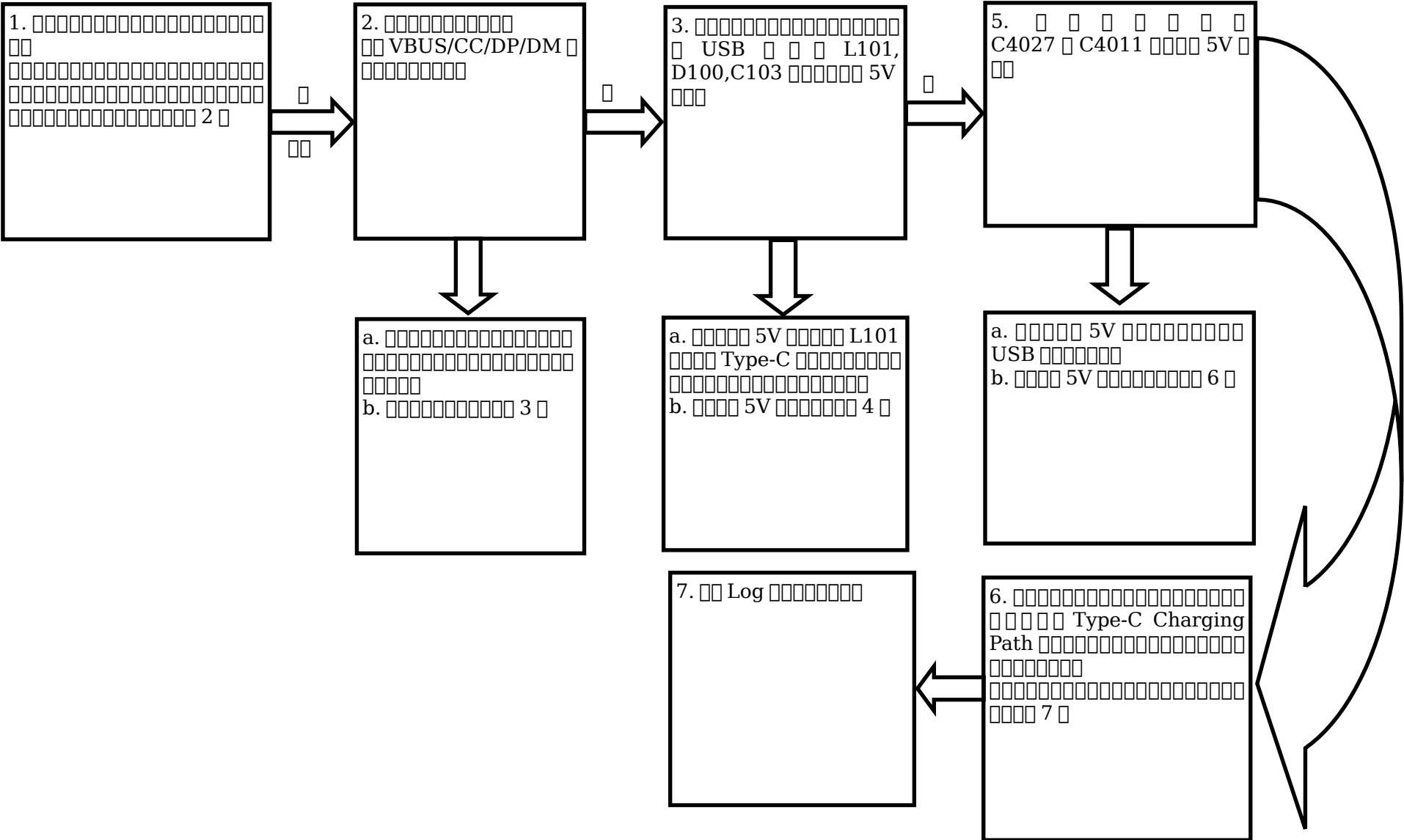
Type-C □□□□□□

Type-C Charging Path



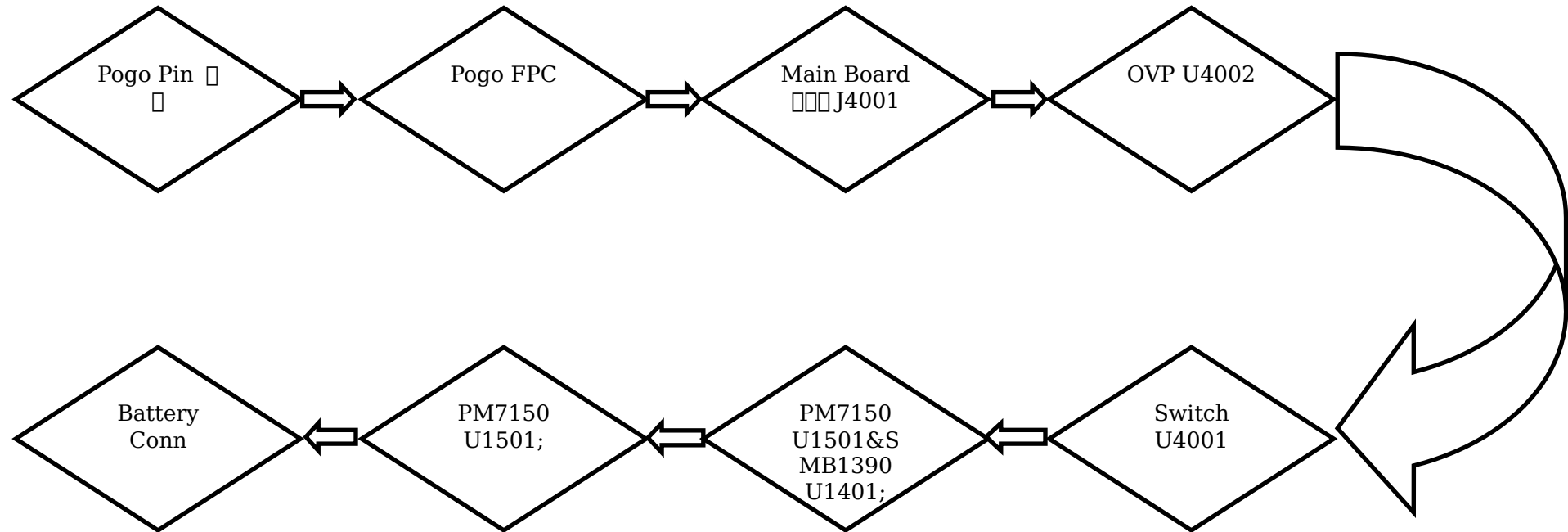
Type-C

Steps



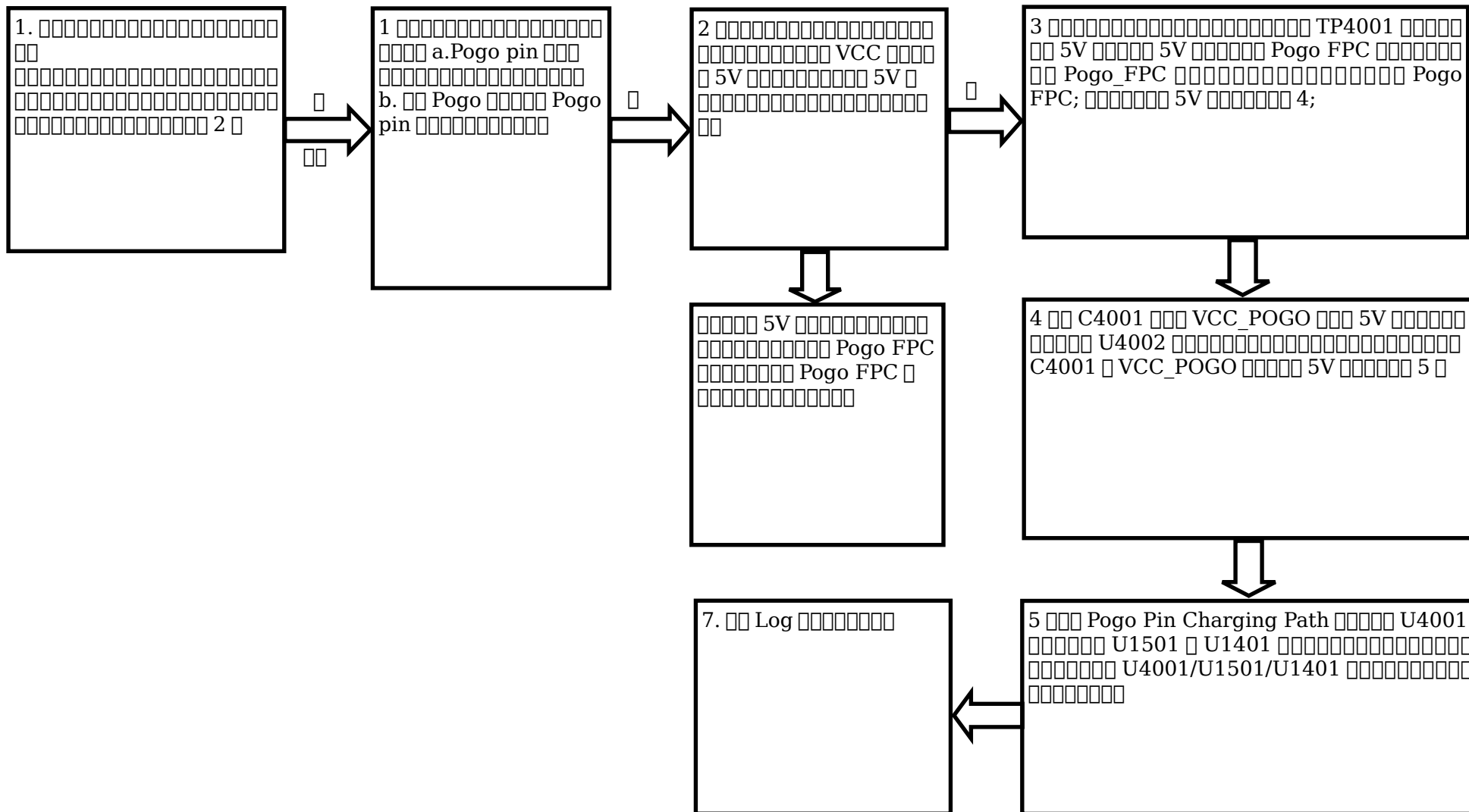
Pogo □□□□□□

Pogo Pin Charging Path



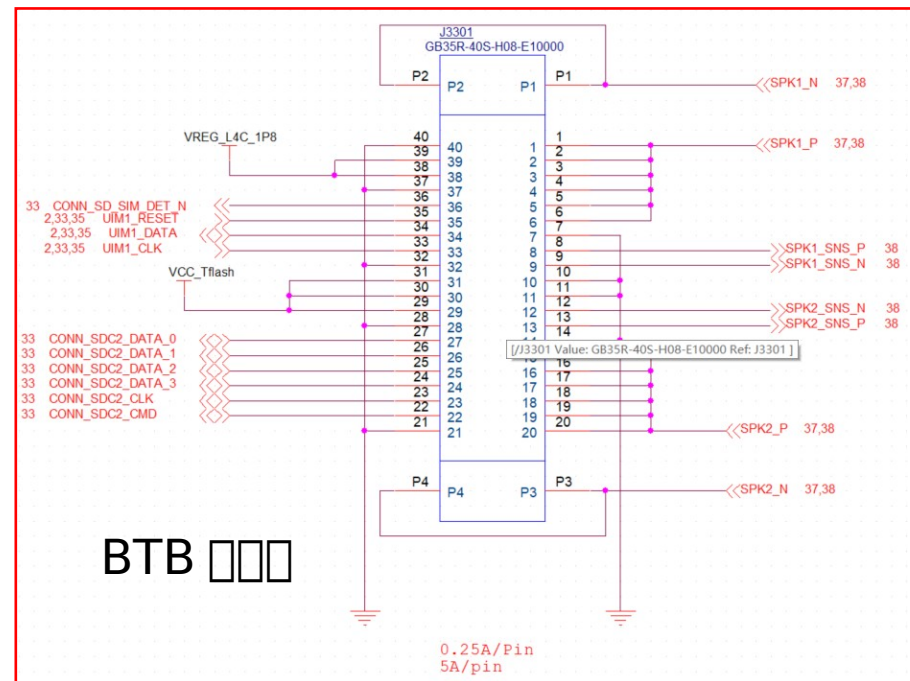
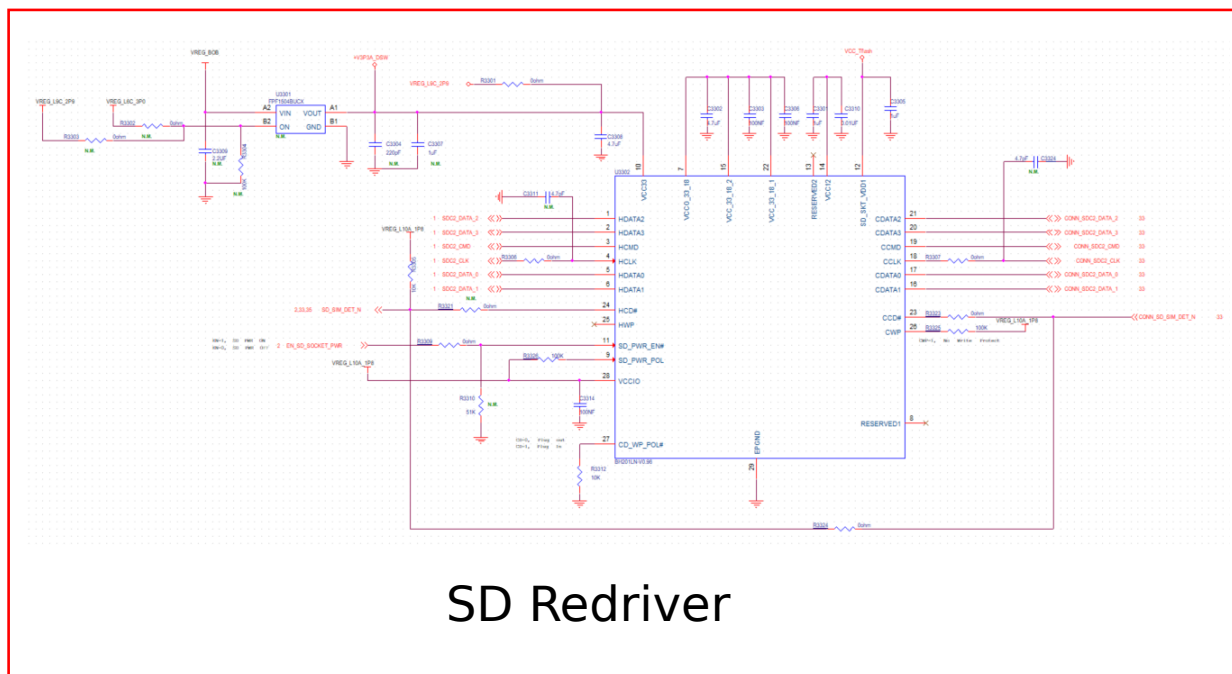
Pogo 测试流程图

Steps

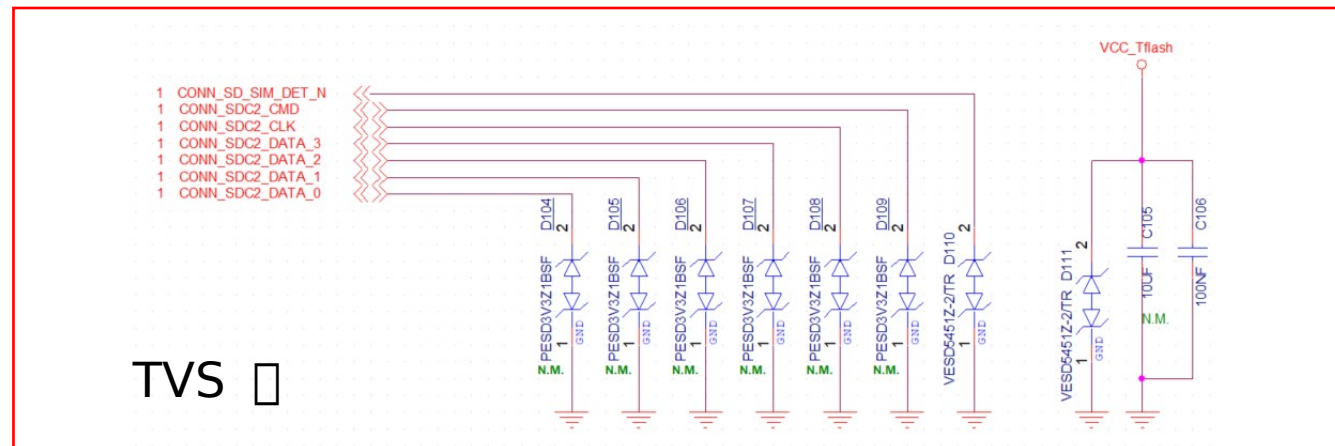
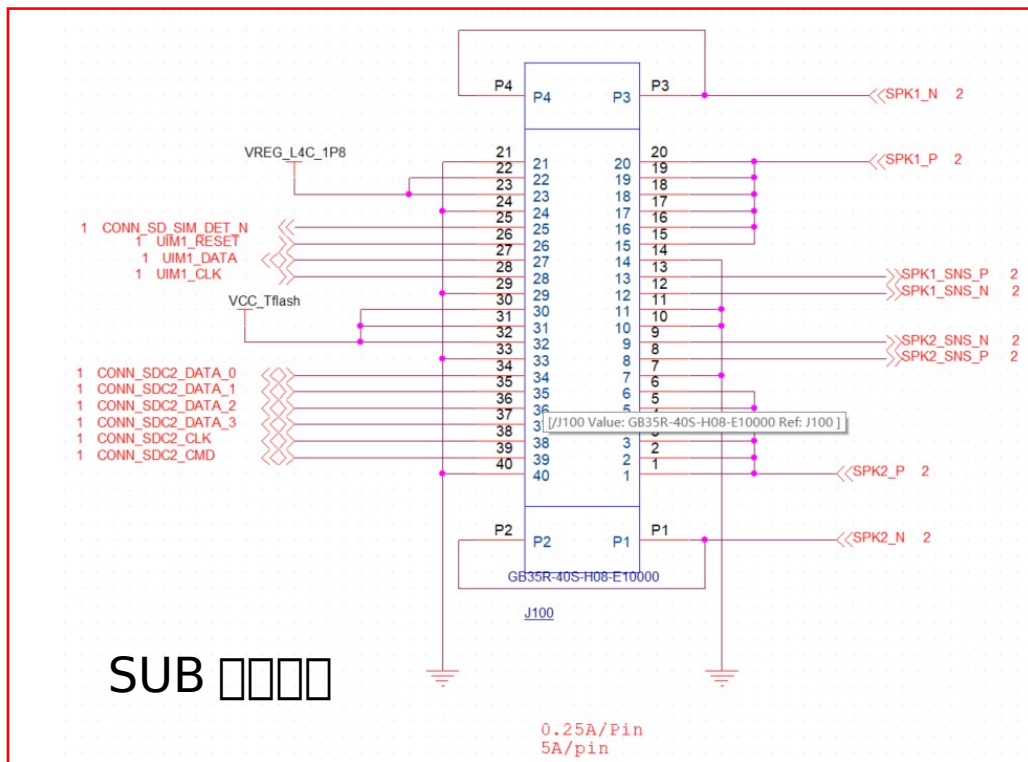


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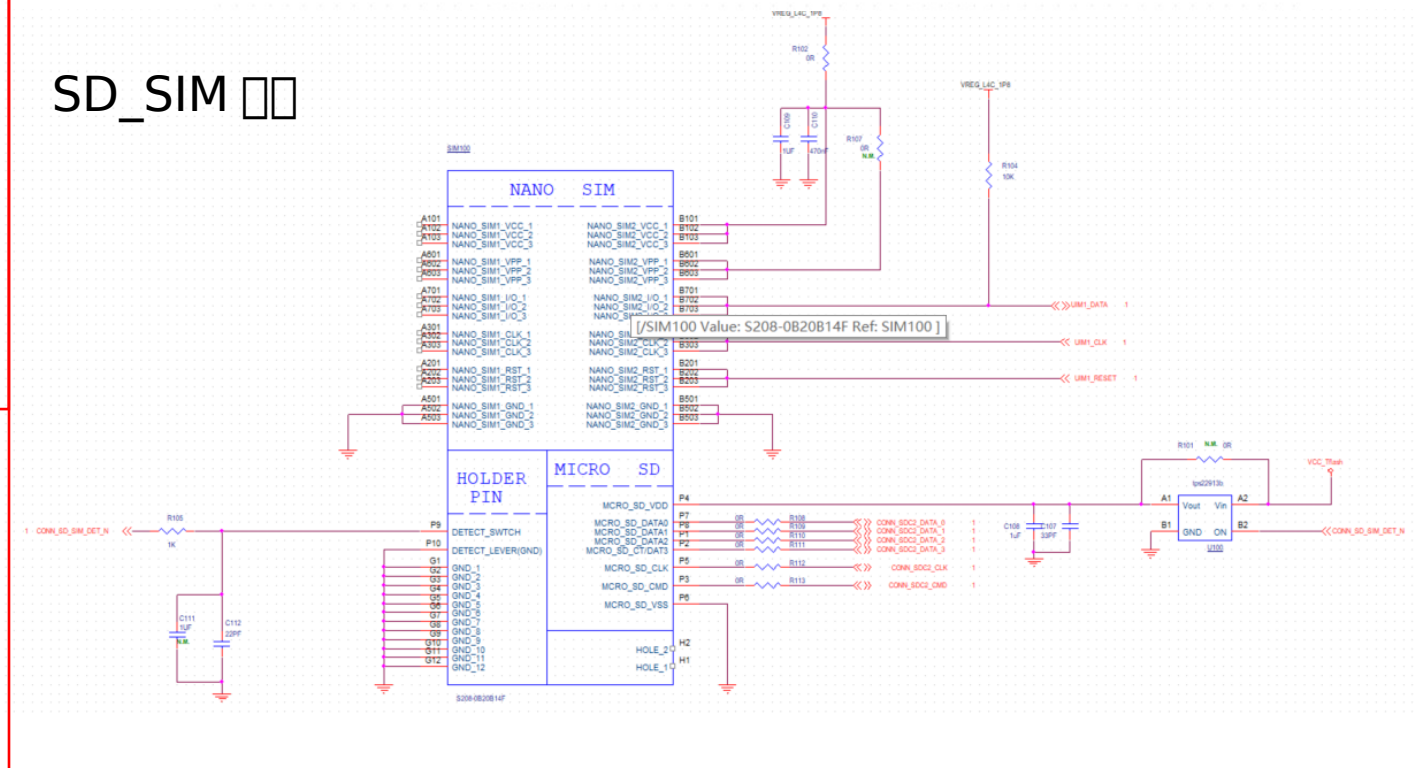
- SD ☐☐☐☐☐☐☐☐
- SD ☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐☐



- SD
- SD

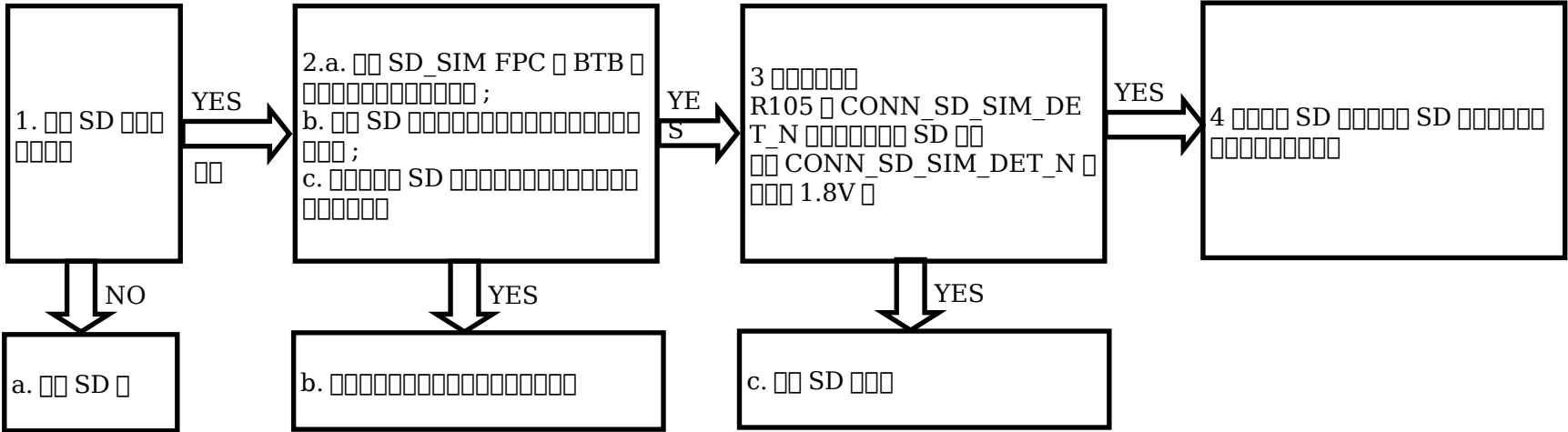


SD_SIM



SD 检查步骤

Steps



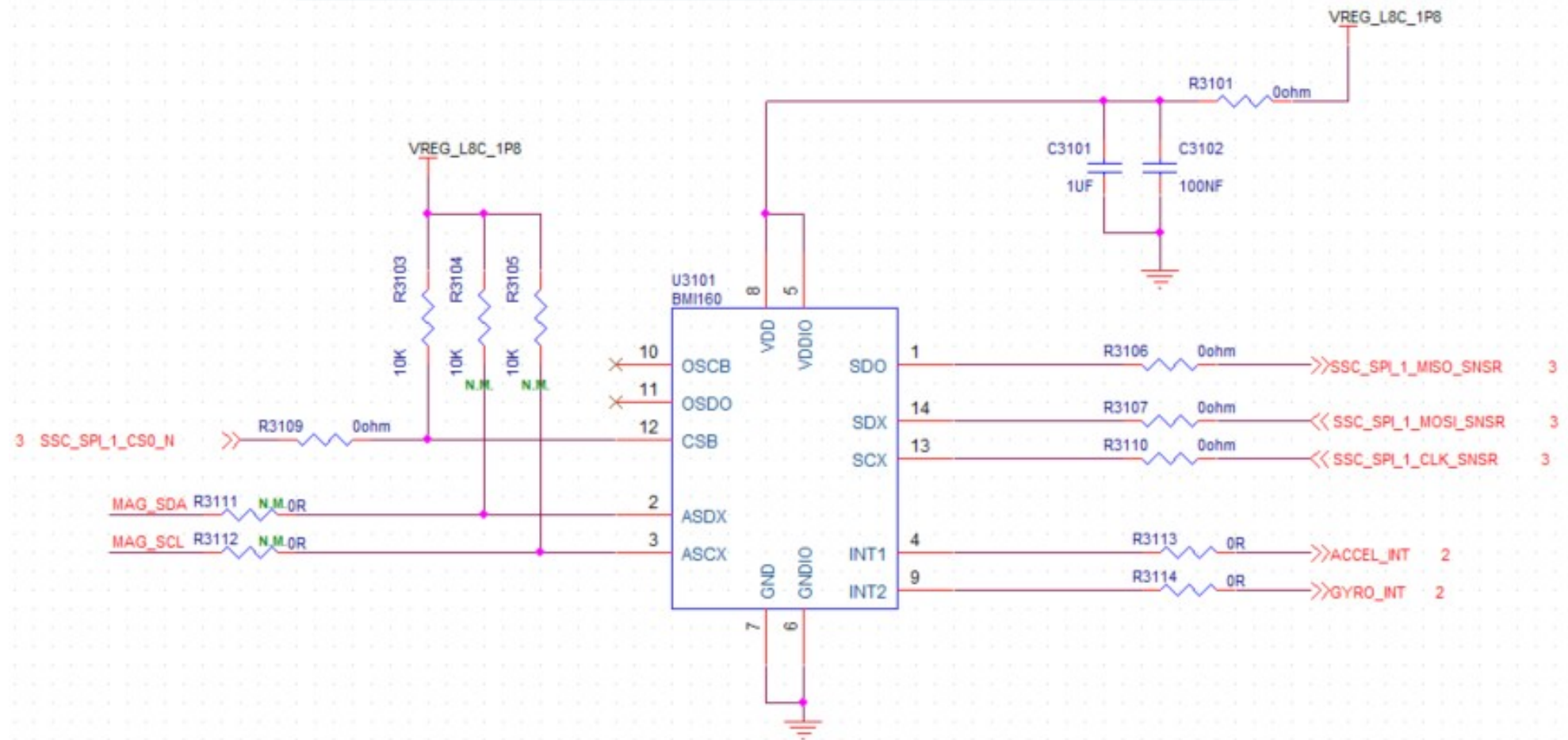
检查 SD 卡是否插入 SD_SIM 检查 SD_SIM 检查 SD_SIM FPC 检查 SD 卡是否插入 FPC 检查 SD 卡是否插入 CONN_SD_SIM_DET_N 检查 SD 卡是否插入 FPC 检查 SD 卡是否插入

5. Sensors □□□□

5. Sensors

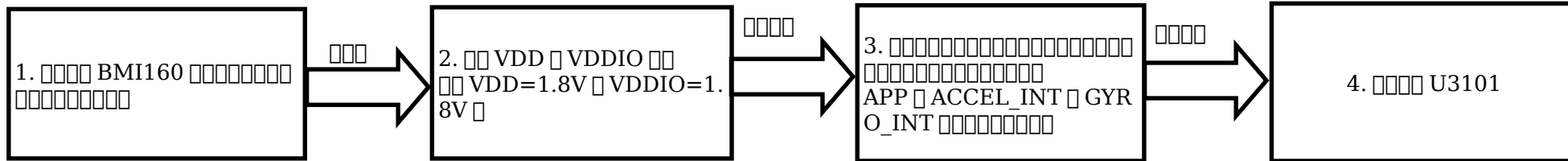
+ :

ACCELEROMETER+GYRO



Sensors 传感器

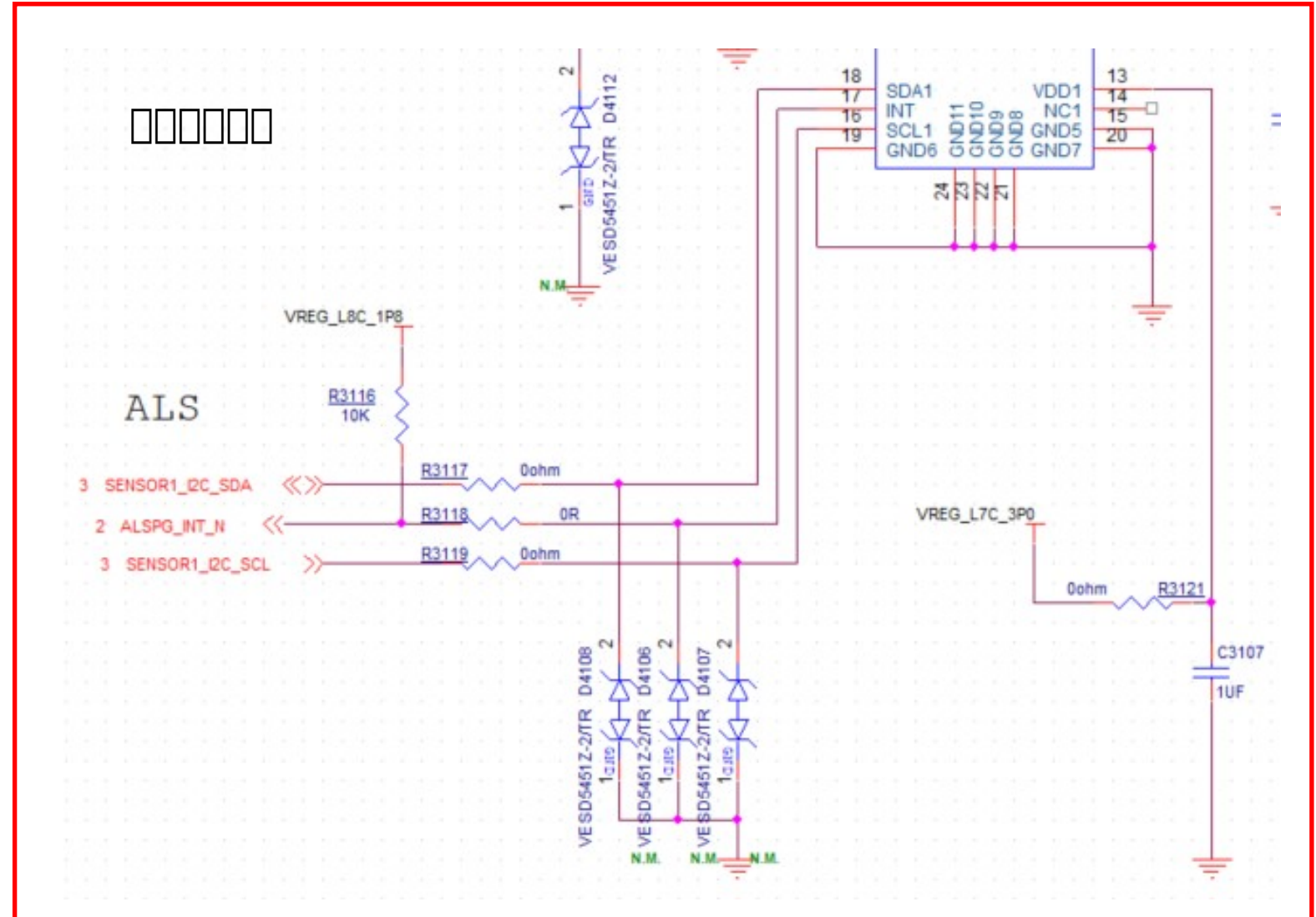
Steps



Sensors

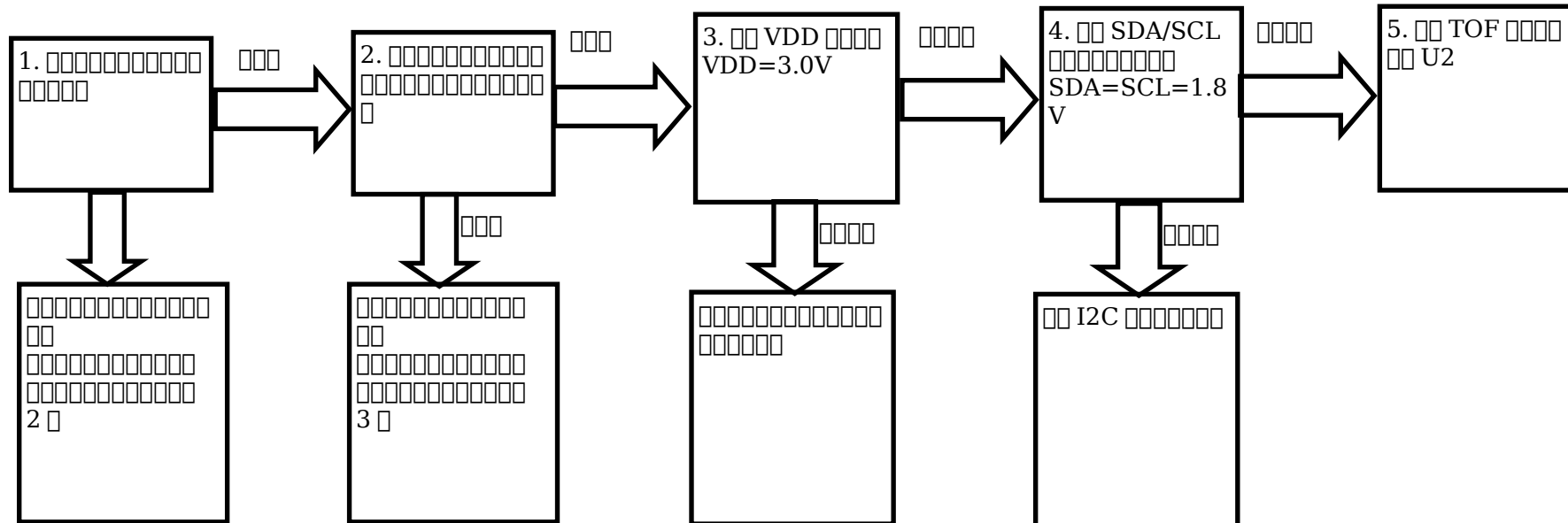
：

TOF
Pin Pin



Sensors

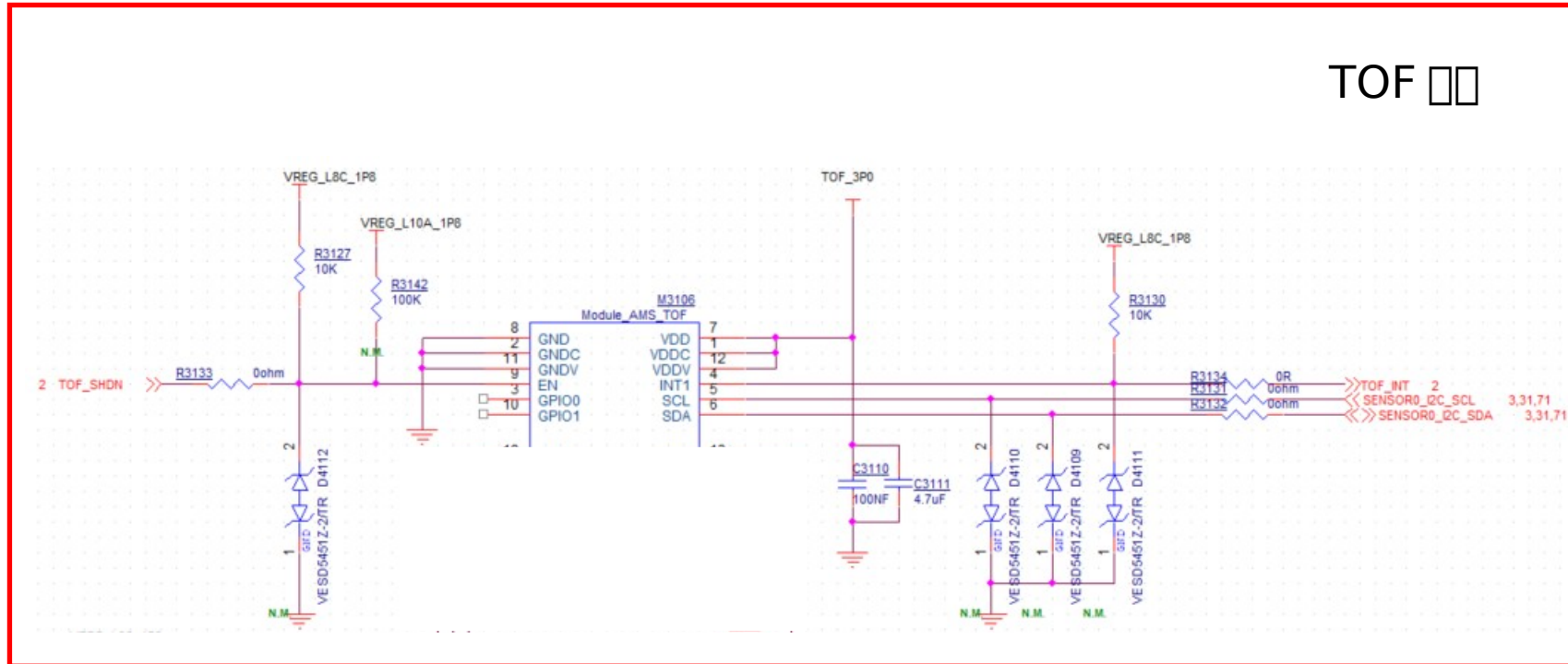
Steps



Sensors □□□□

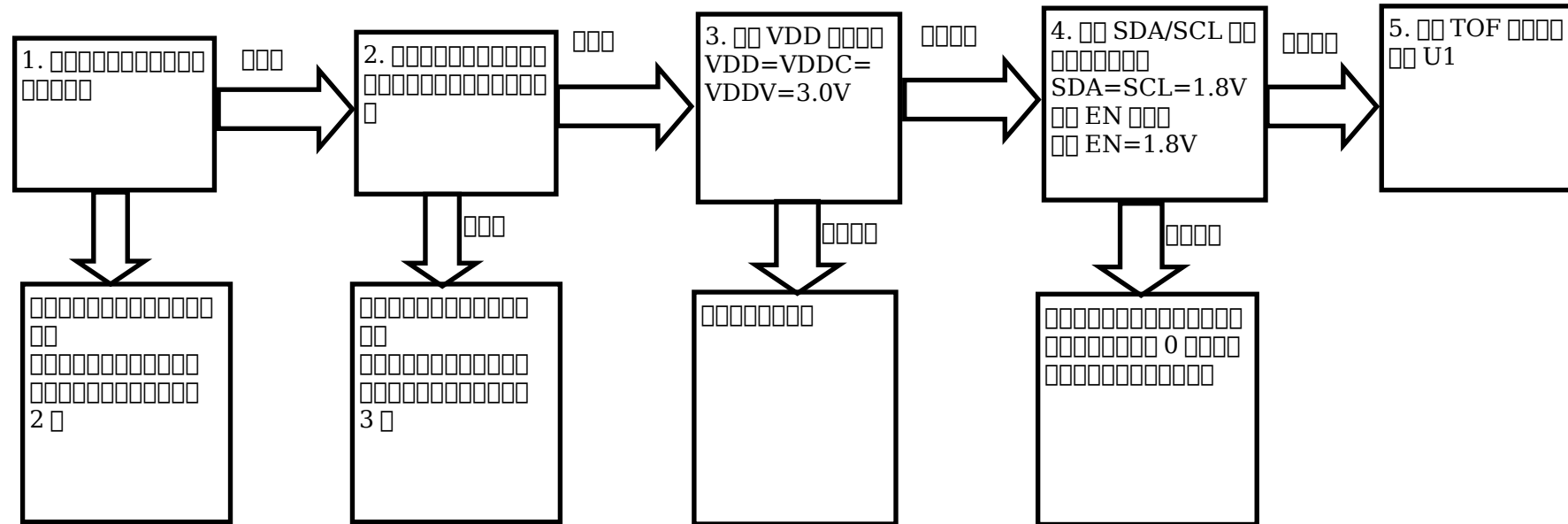
TOF □□□□□□□ :

TOF □□



Sensors □□□□

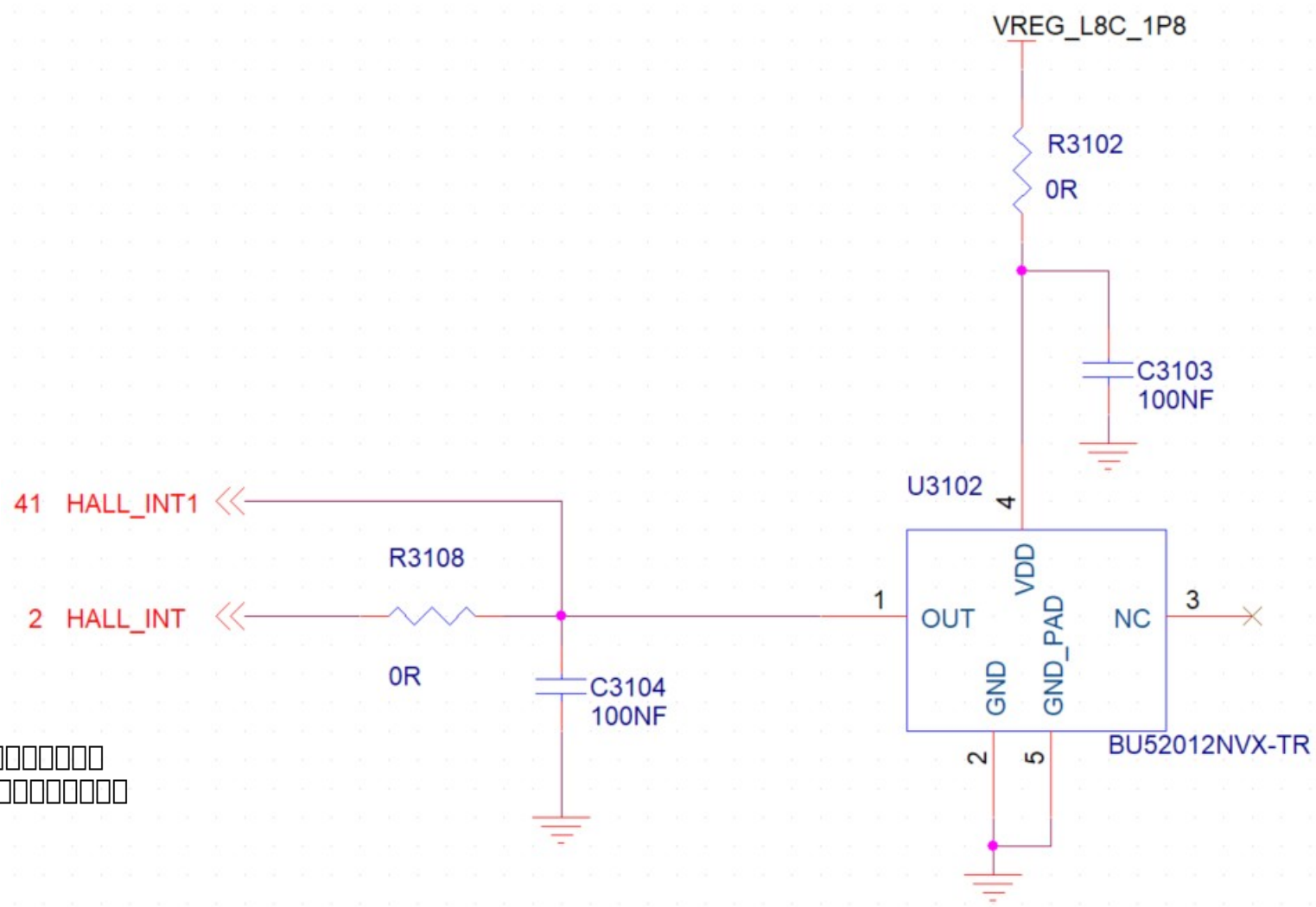
Steps



Sensors □□□□

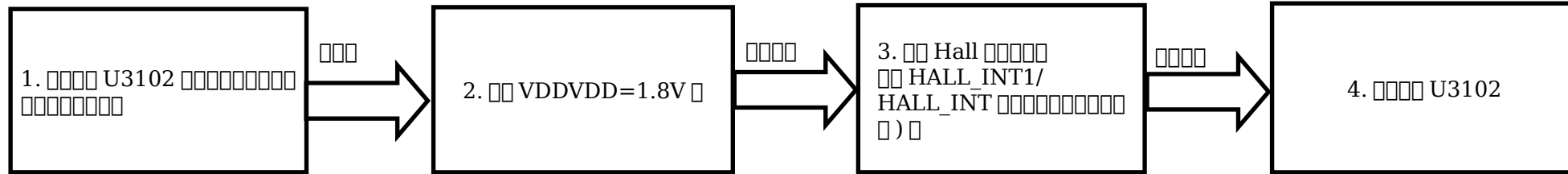
HALL □□□□□□□□ :

□□□
□□□□□ TOF □□□□□□□□□□□□
□□□ Pin □□□□□ Pin □□□□□□□□□□
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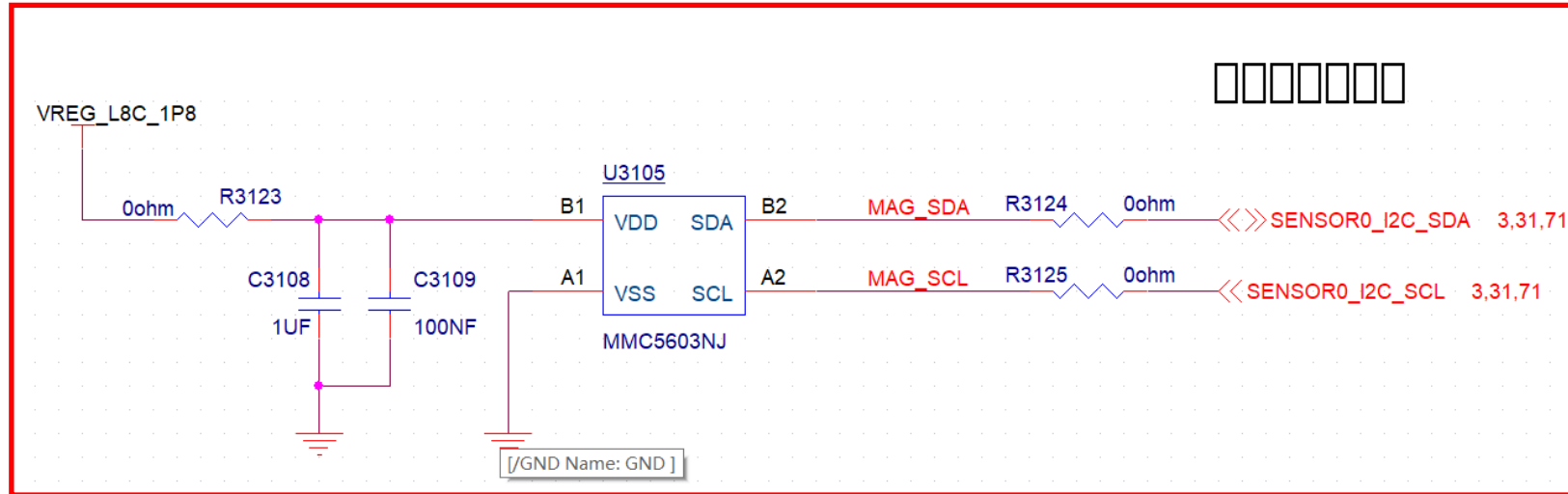
Sensors

Steps



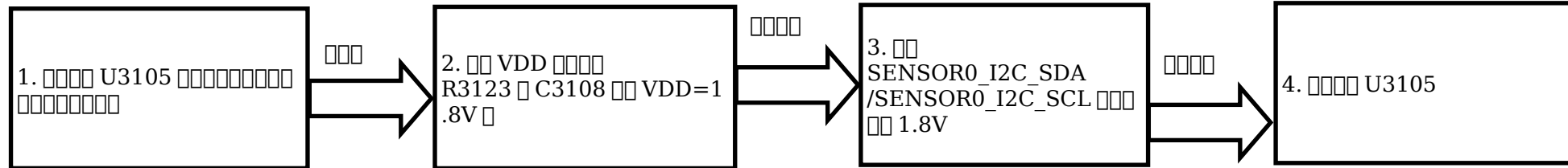
Sensors

：



Sensors □□□□

Steps



6. □□□□□□

□□□□□□

TP □□□□ :

TP I2C □□□□

VREG_L10A_1P8

R307
2K

R308
2K

2,29 TS_I2C_SDA<>>

2,29 TS_I2C_SCL >>

29 TP_RST
2,3,29 TP_SCL
2,3,29 TP_SDA
2,29 TP_INT

VREG_L10A_1P8

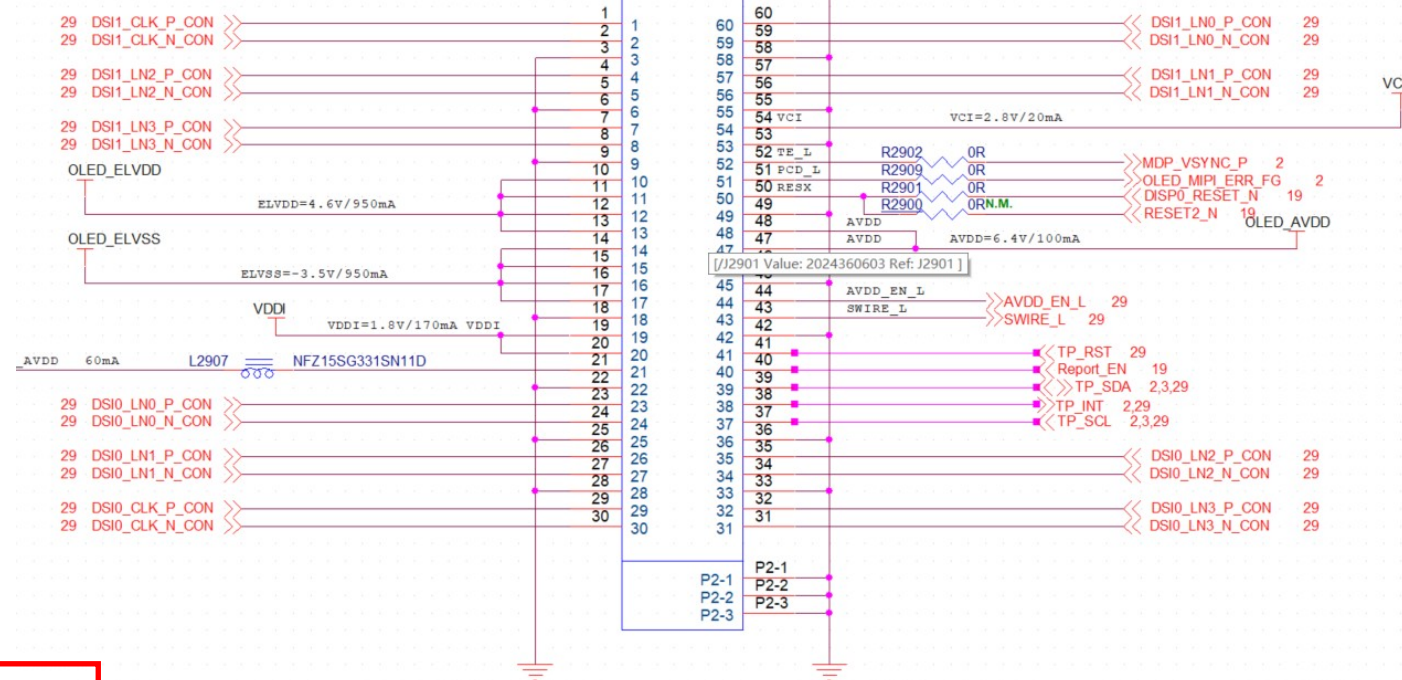
R2903
10K

R2904
10K

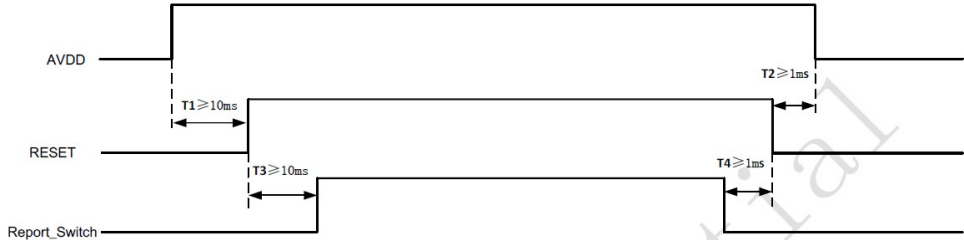
N.M.

TVS □

AMOLED&TP BTB □□□



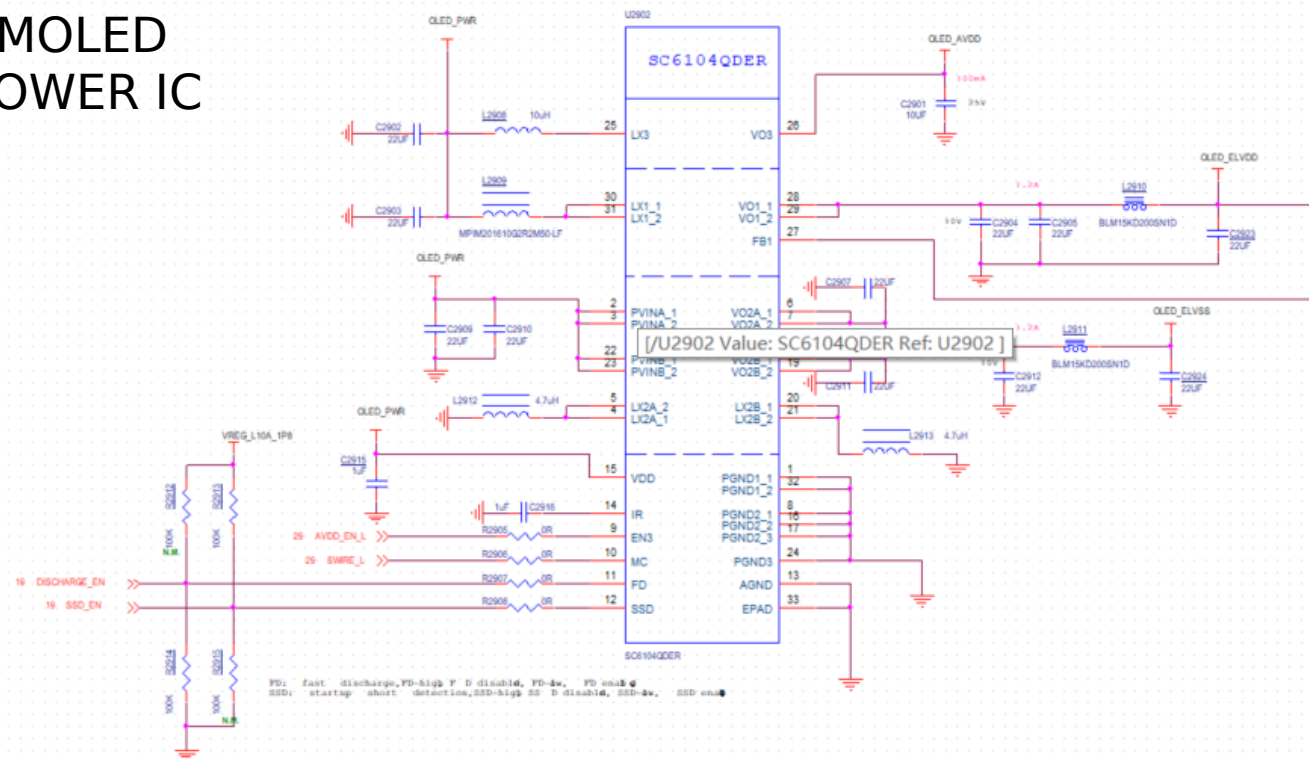
- ## Steps



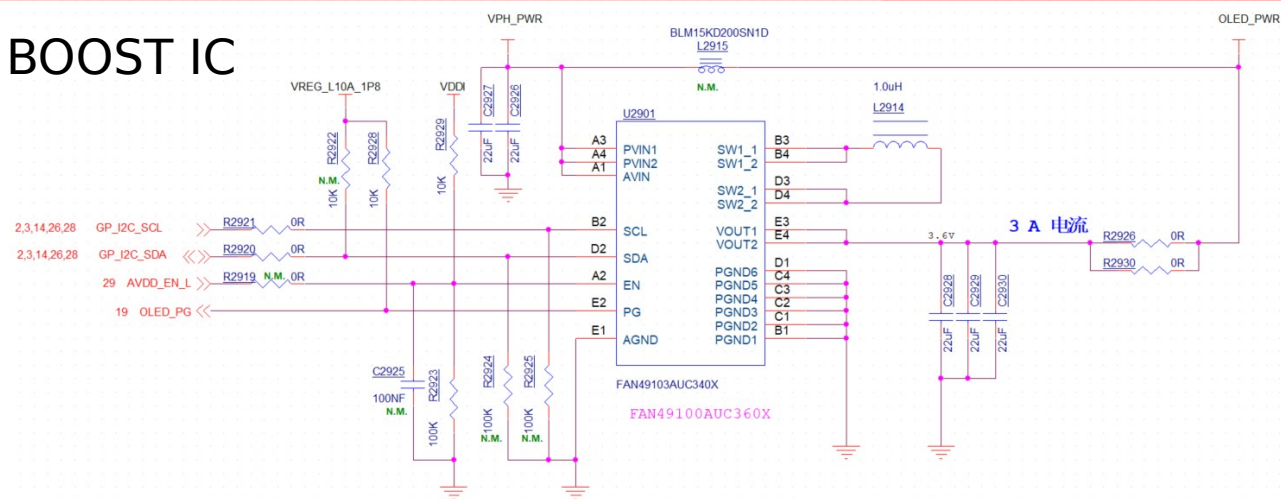
7. □□□□□□□

□□□□□□□□
LCD □□□□ :

AMOLED POWER IC



BOOST IC

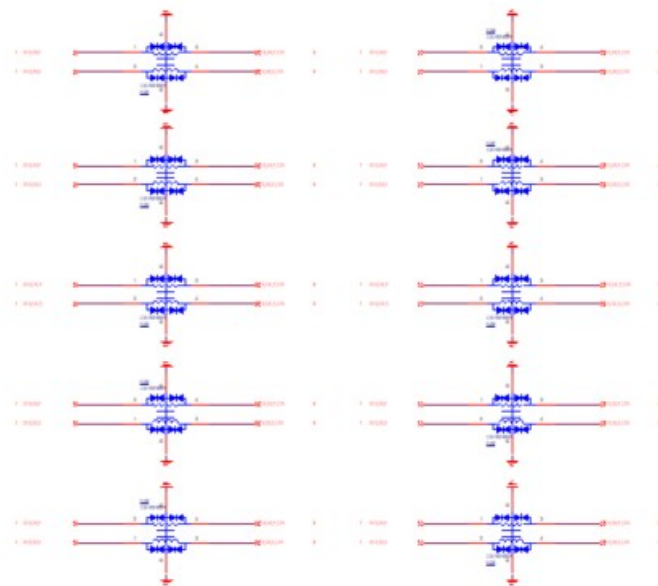


Address: 0xE0 (w) / 0xE1 (r)

LCD :

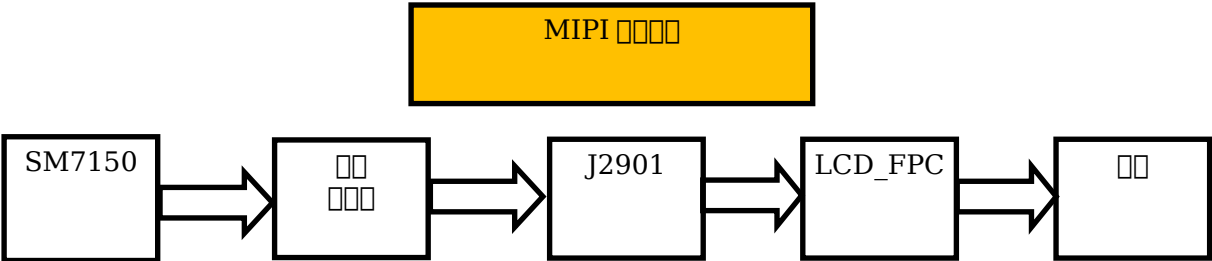
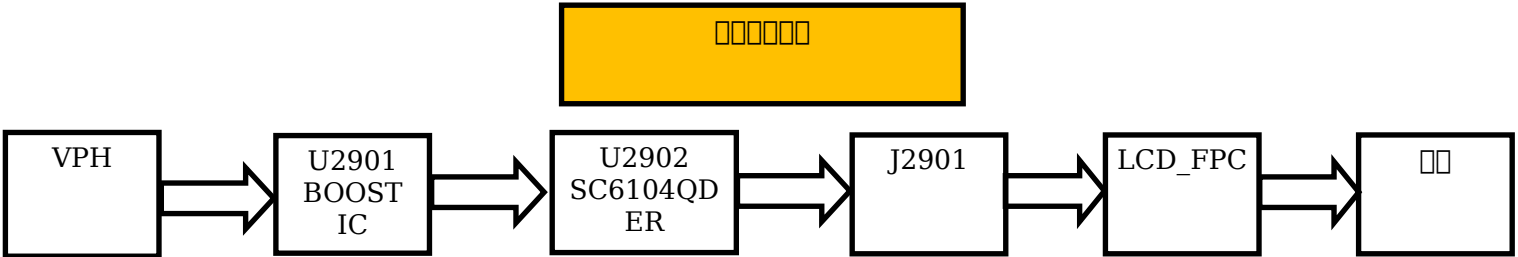


11' Main LCD Connector



□□□□□□□

Steps





Steps

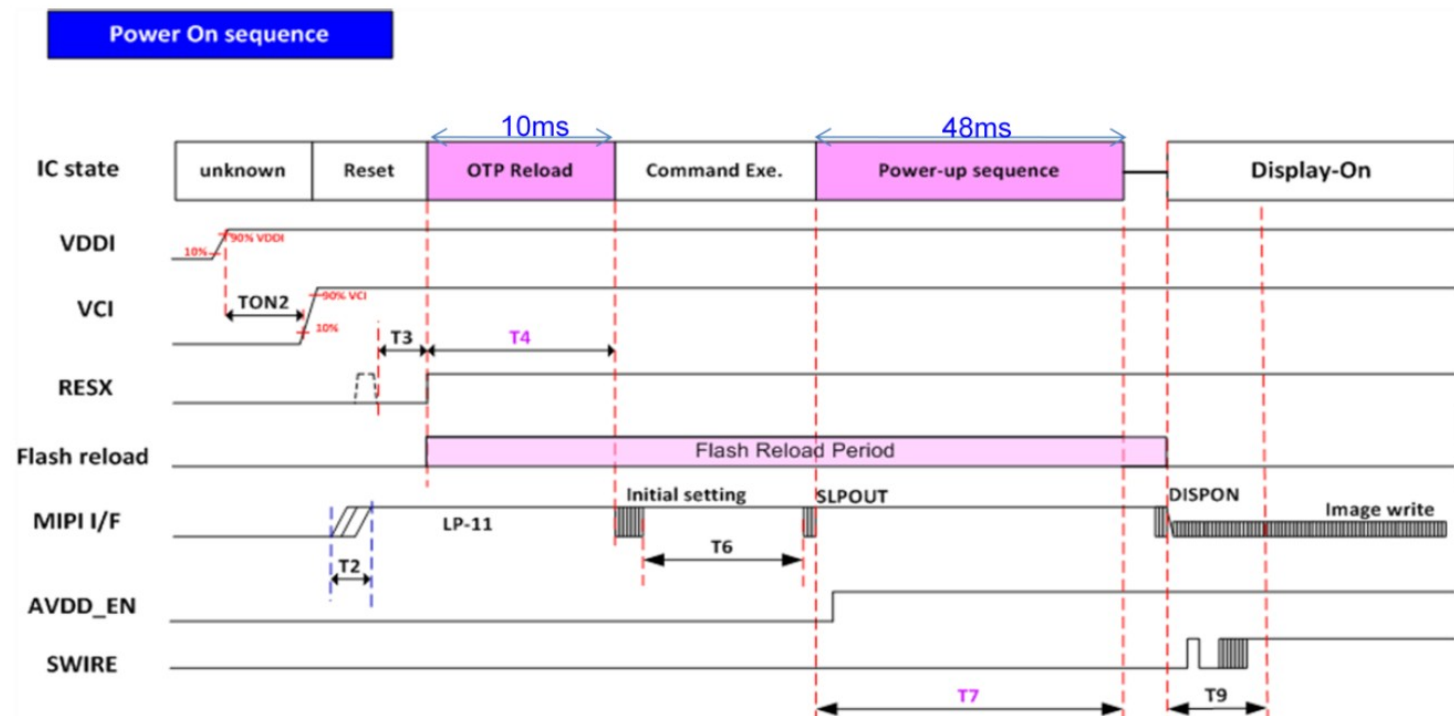
1. LCD J2901

2. LCD

3. LCD

4. LCD

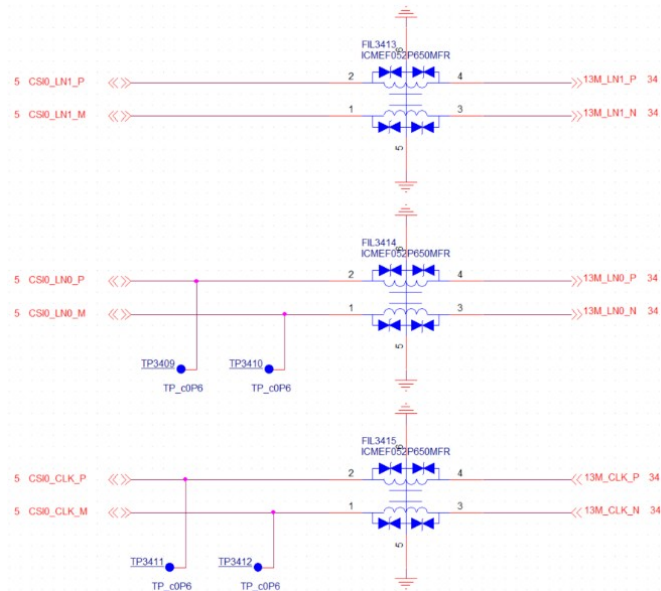
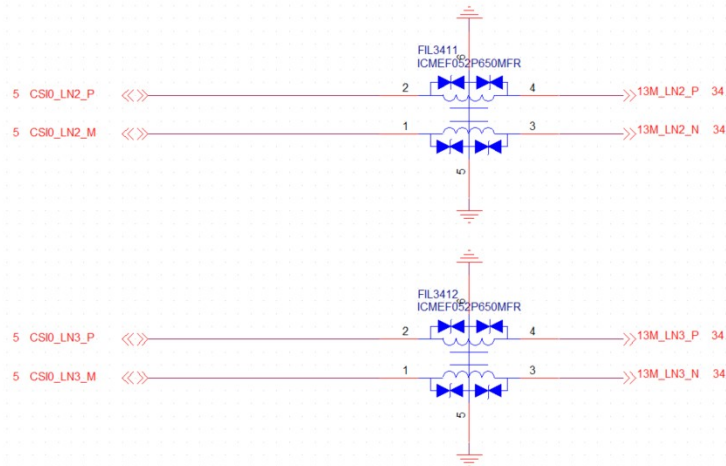
Power Sequence and Generation



8. □□□□□□□

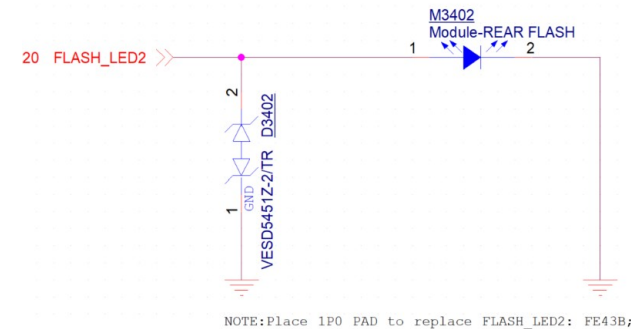
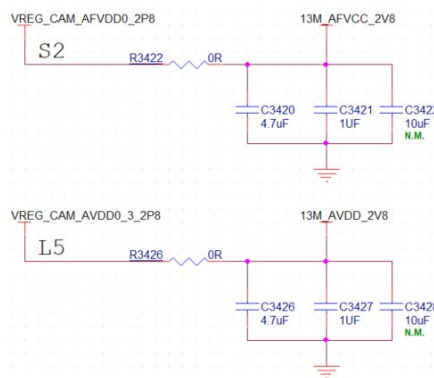
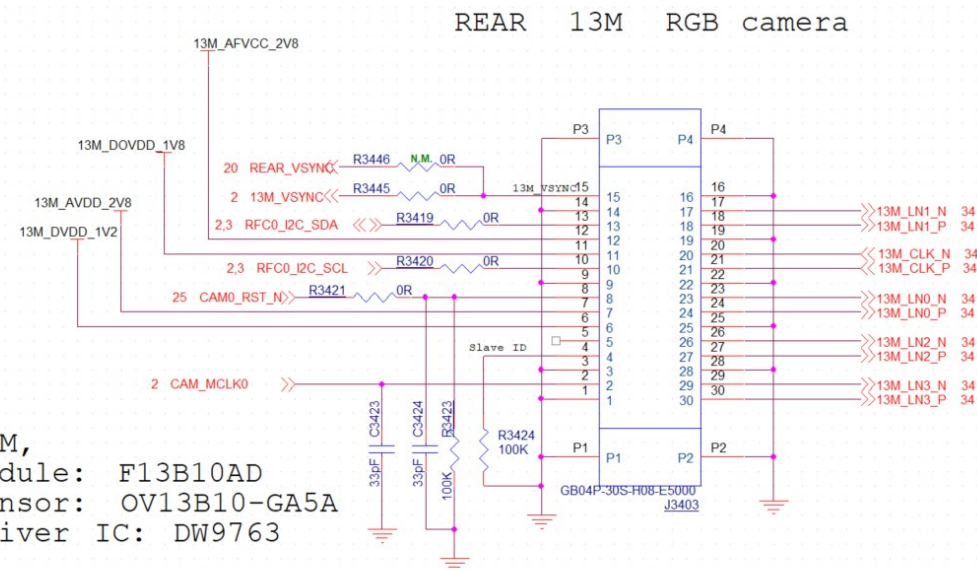
□□□□□□□□

13M □□□□□□□□ :



13M,
Module: F13B10AD
Sensor: OV13B10-GA5A
Driver IC: DW9763

OV13B10 Address: 0x6c(w)/0x6d(r) VDDA=2.8V, IDDA=30mA;
DW9763 Address: 0x18(w)/0x19(r) VDD=1.2V, IDD=96.5mA;
EEPROM Address: 0xb0(w)/0xb1(r) VDDIO=1.8V, IDDA=;



NOTE: Place IP0 PAD to replace FLASH_LED2: FE43B;

□□□□□□□□

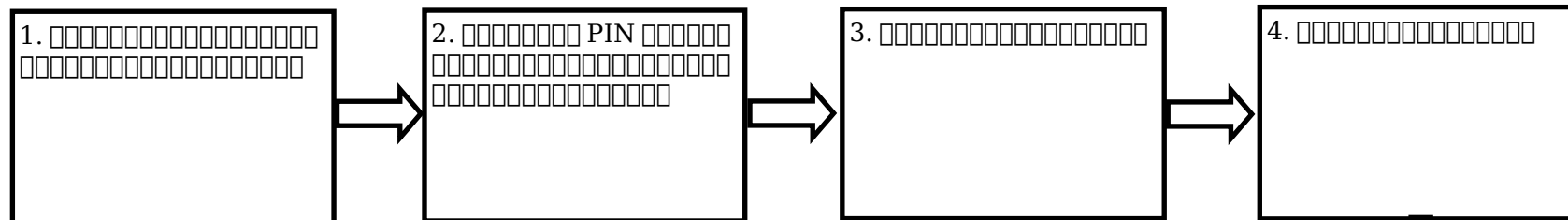
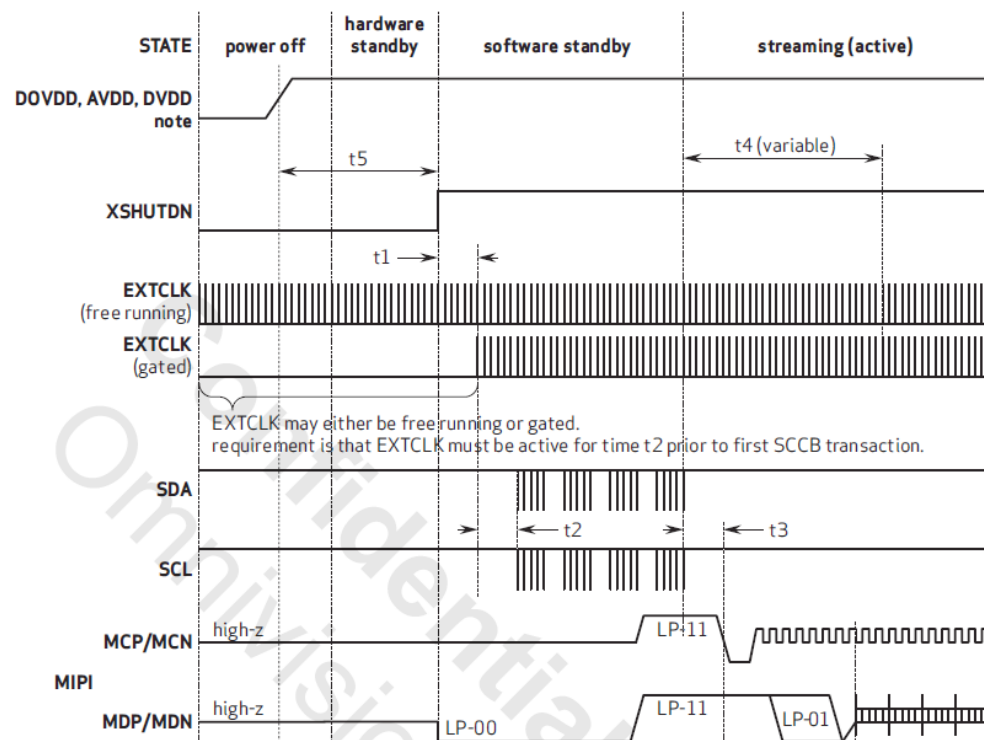
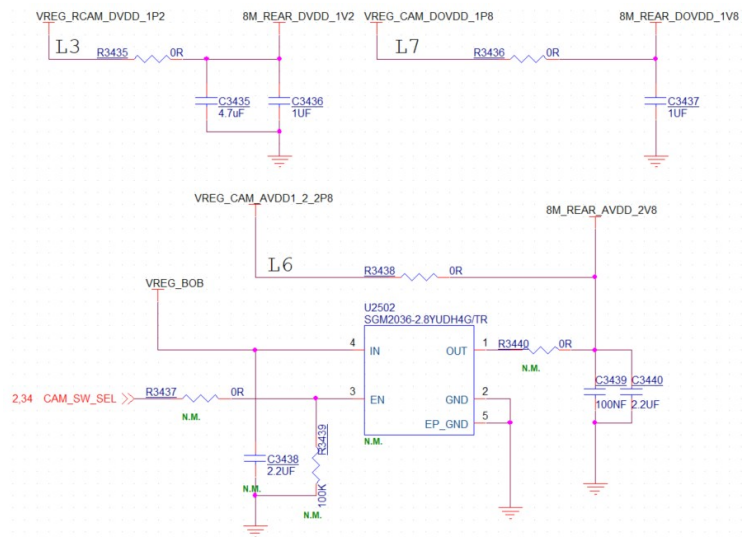
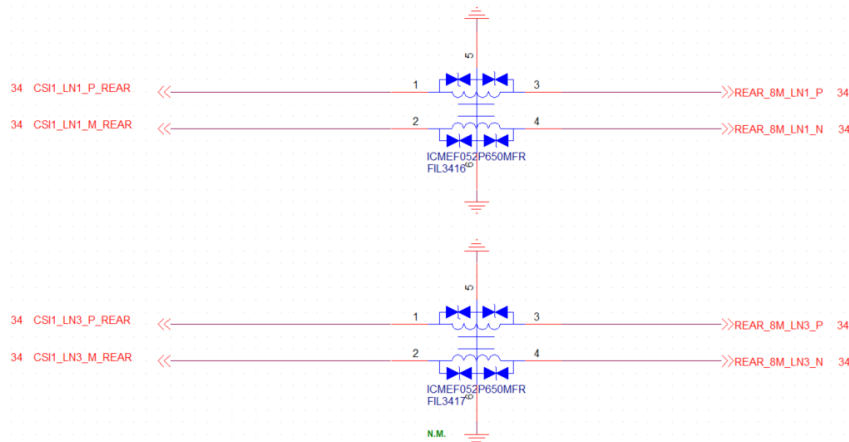
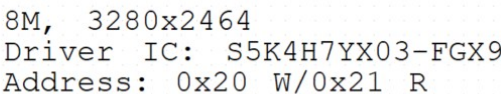


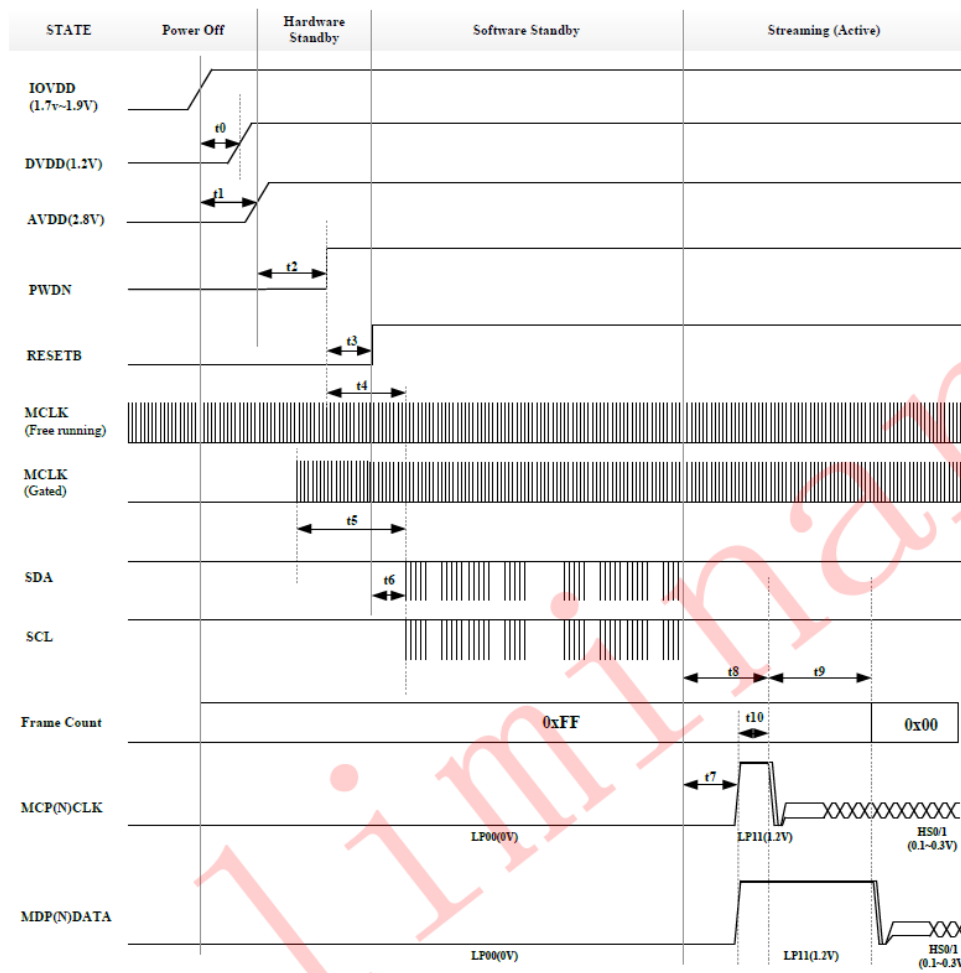
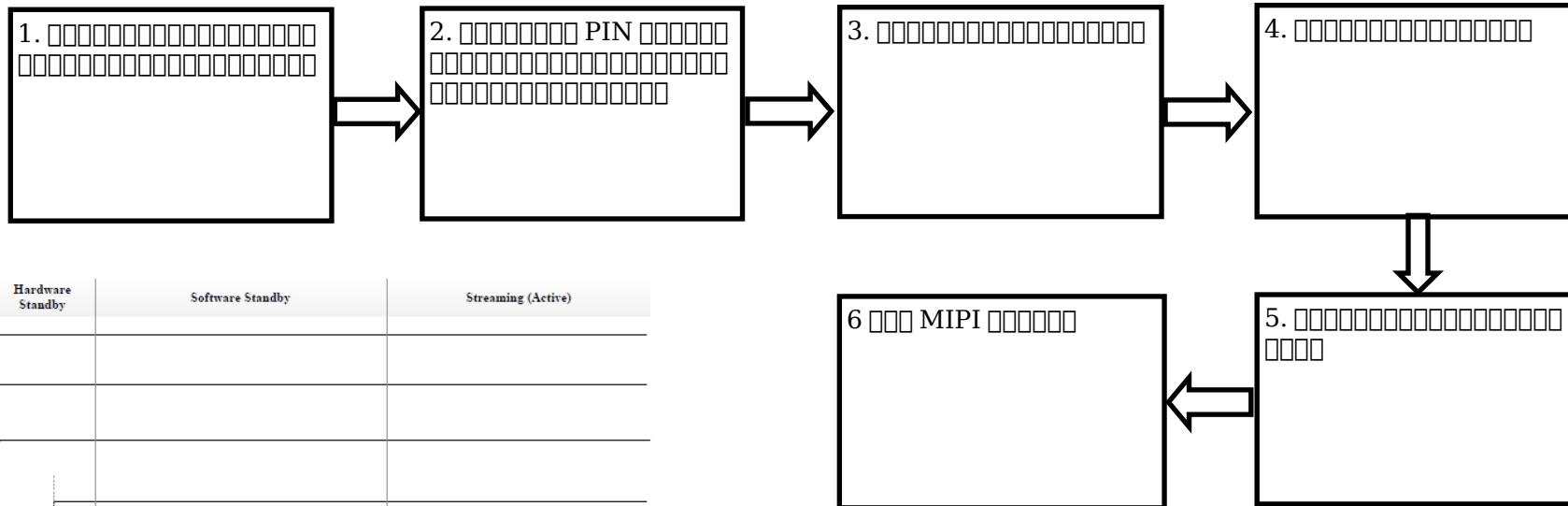
figure 2-3 power up sequence



note DOVDD, AVDD and DVDD may rise in any order

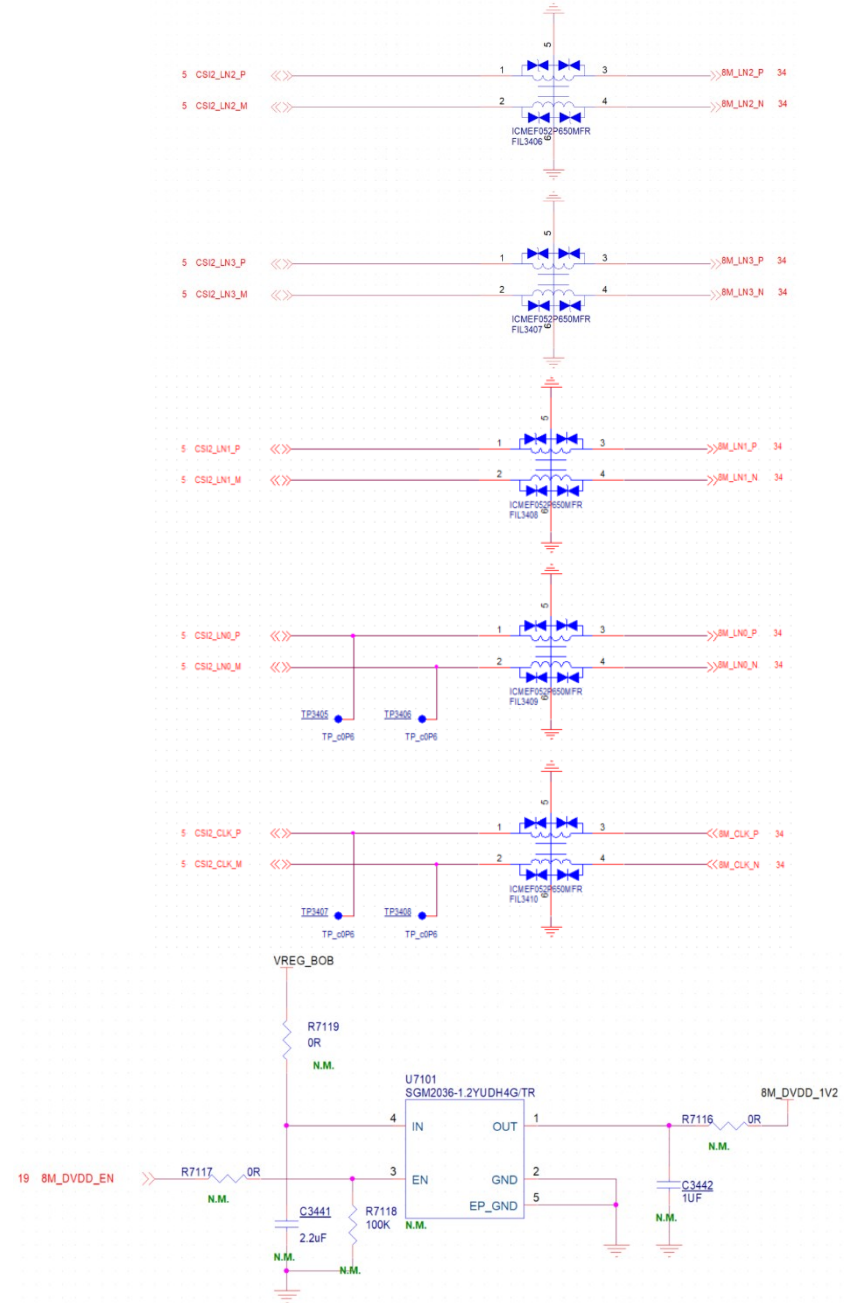
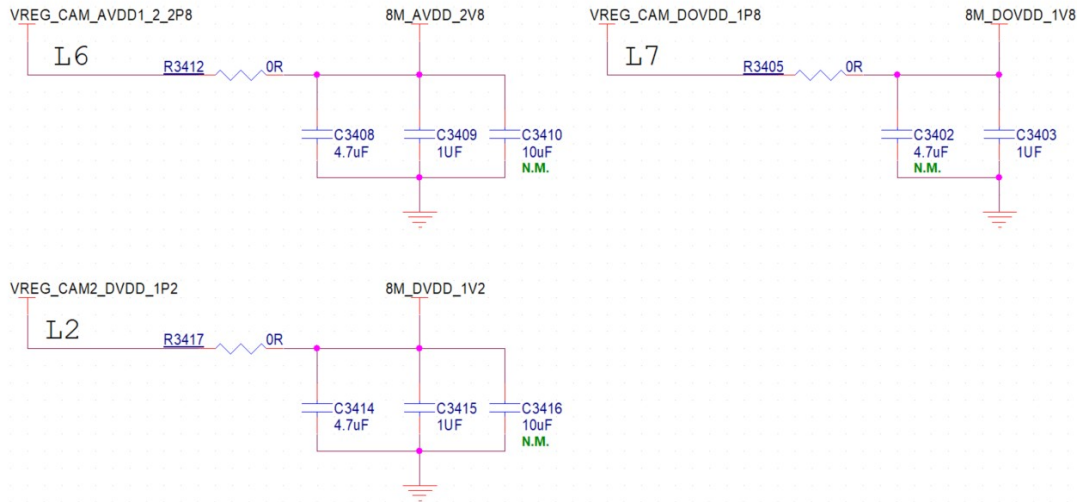
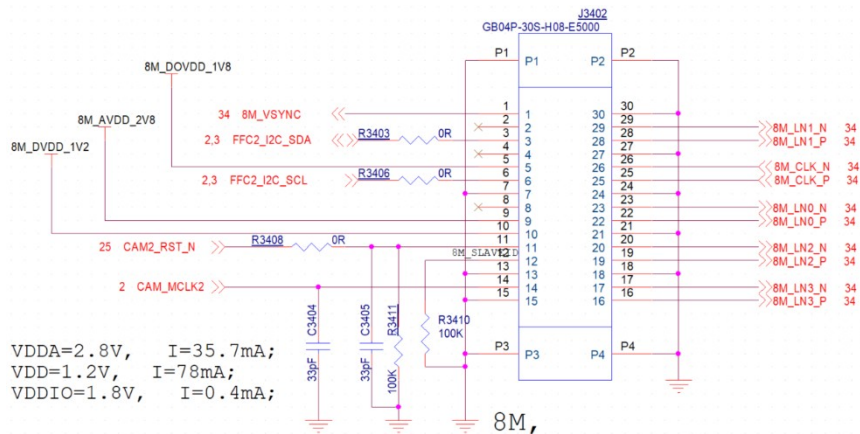
5M □□□□□□□□ :





□□□□□□□□

5M □□□□□□□□ :



5M □□□□□□□□ :

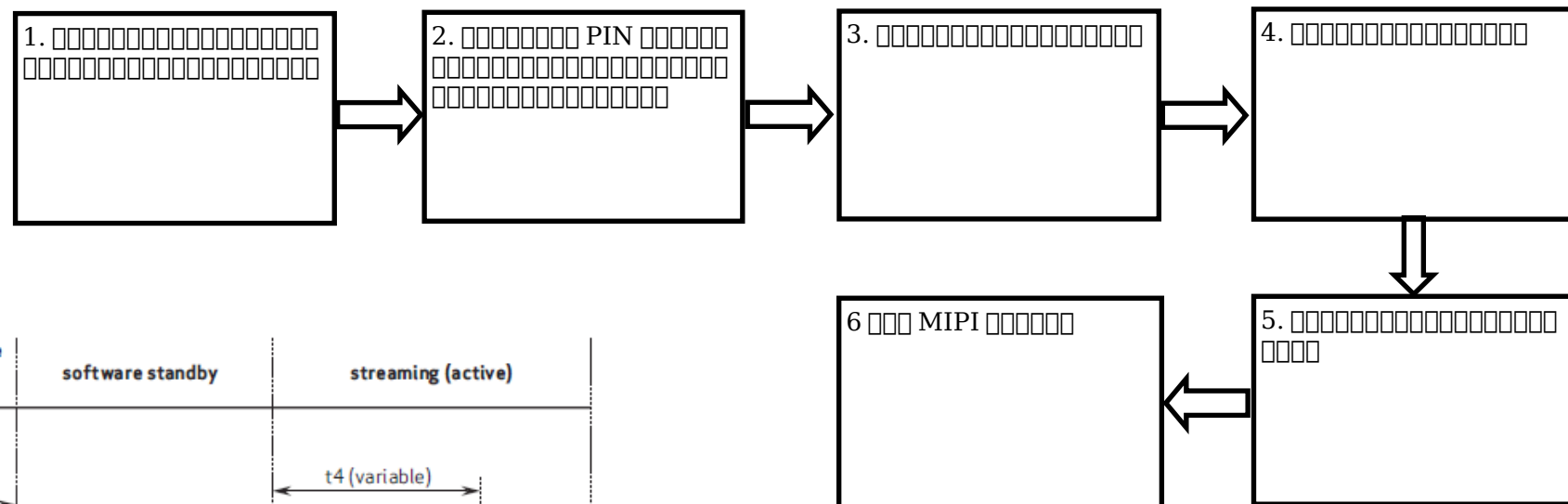
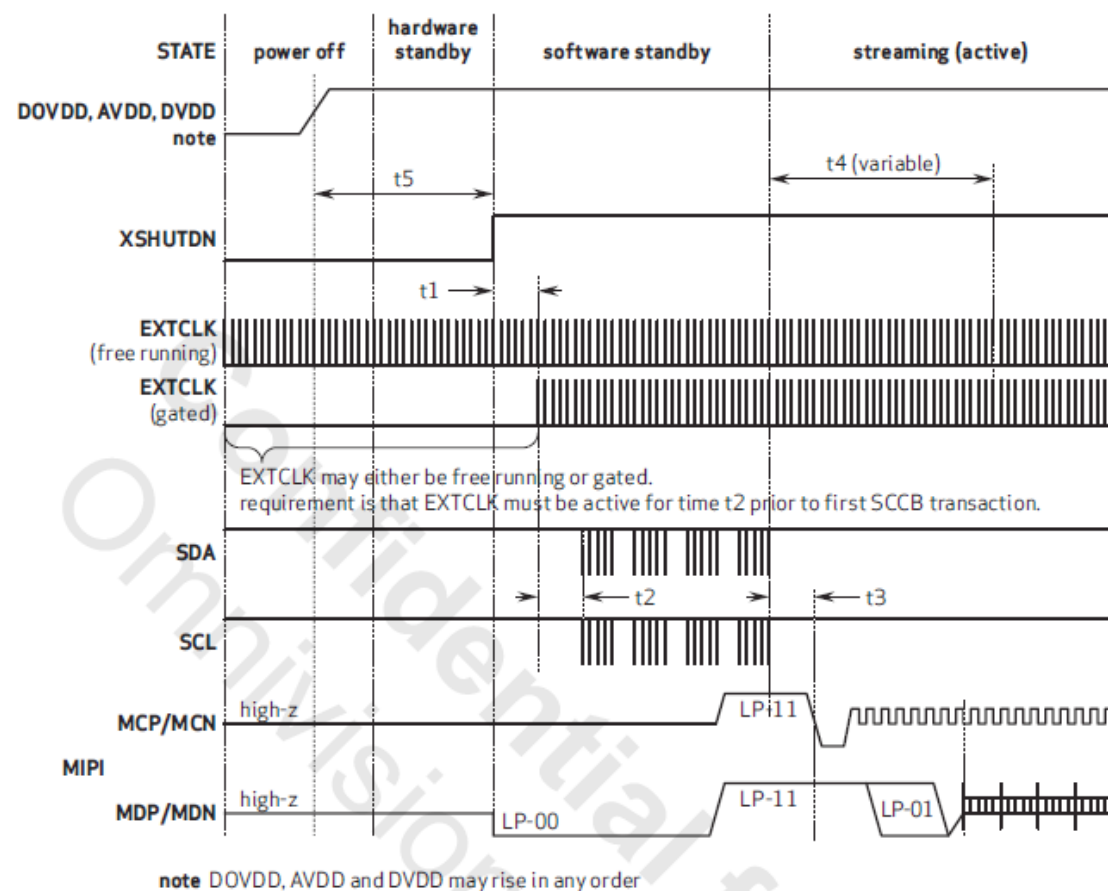
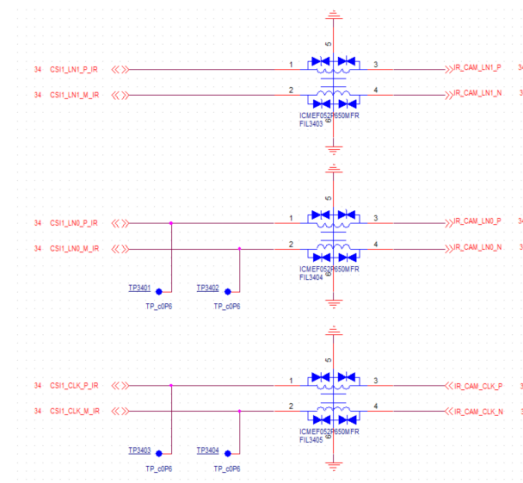
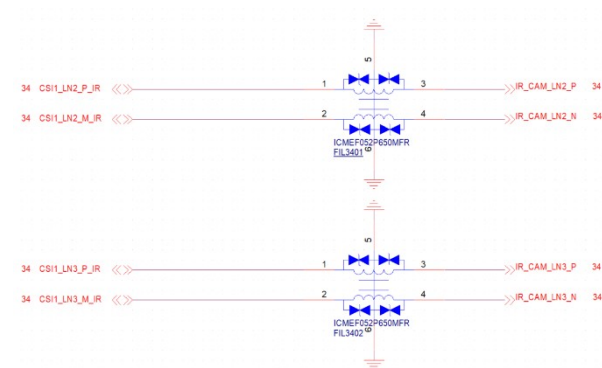
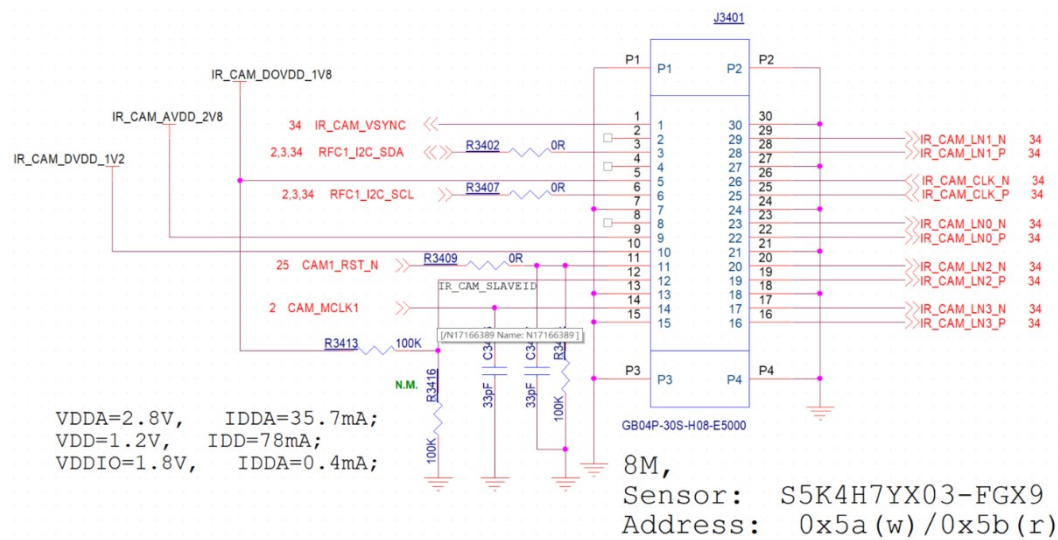


figure 2-3 power up sequence



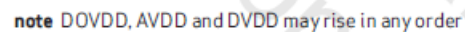
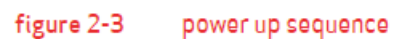
□□□□□□□□

IR □□□□□□□□ :



IR

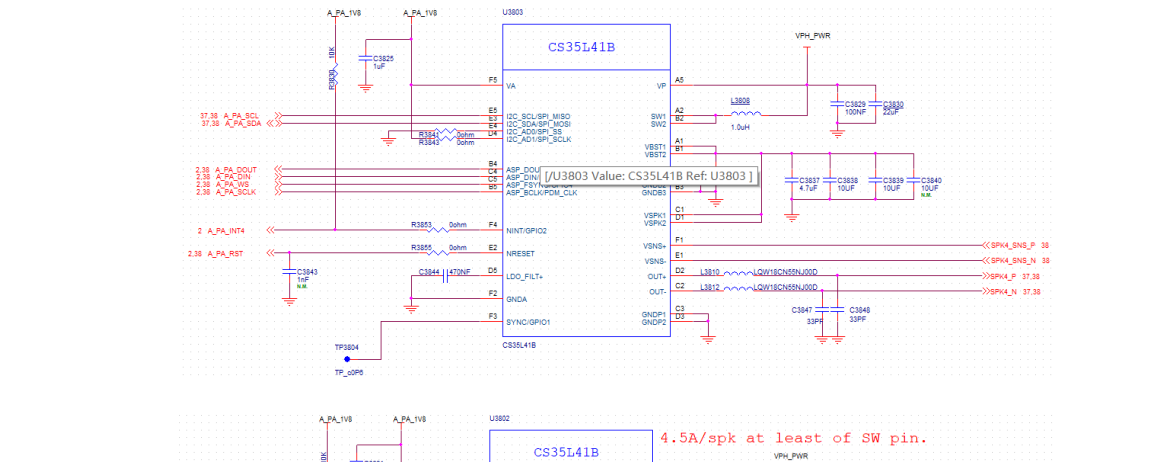
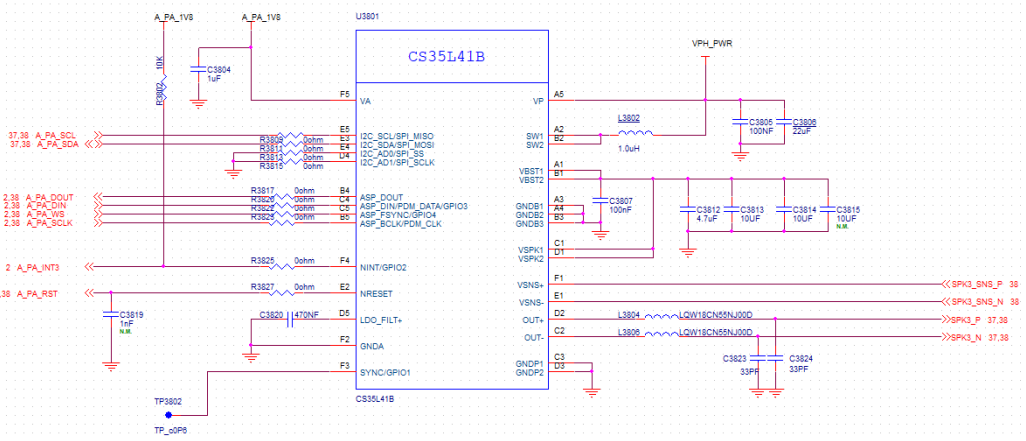
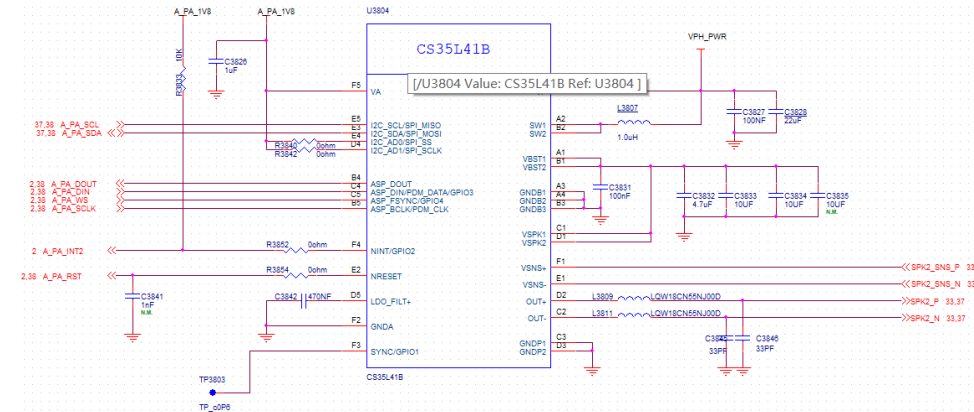
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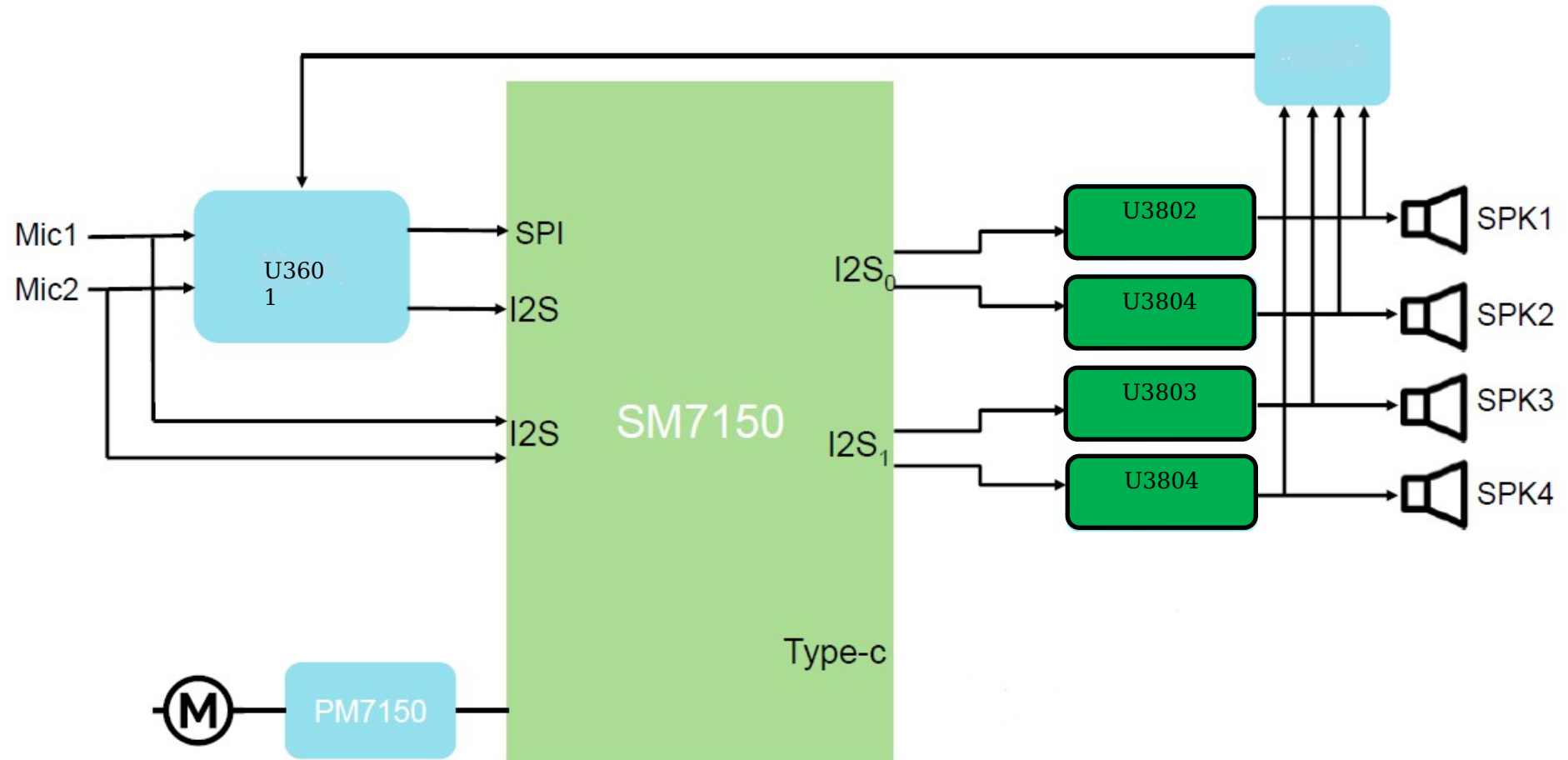
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□□□□□□□□ :

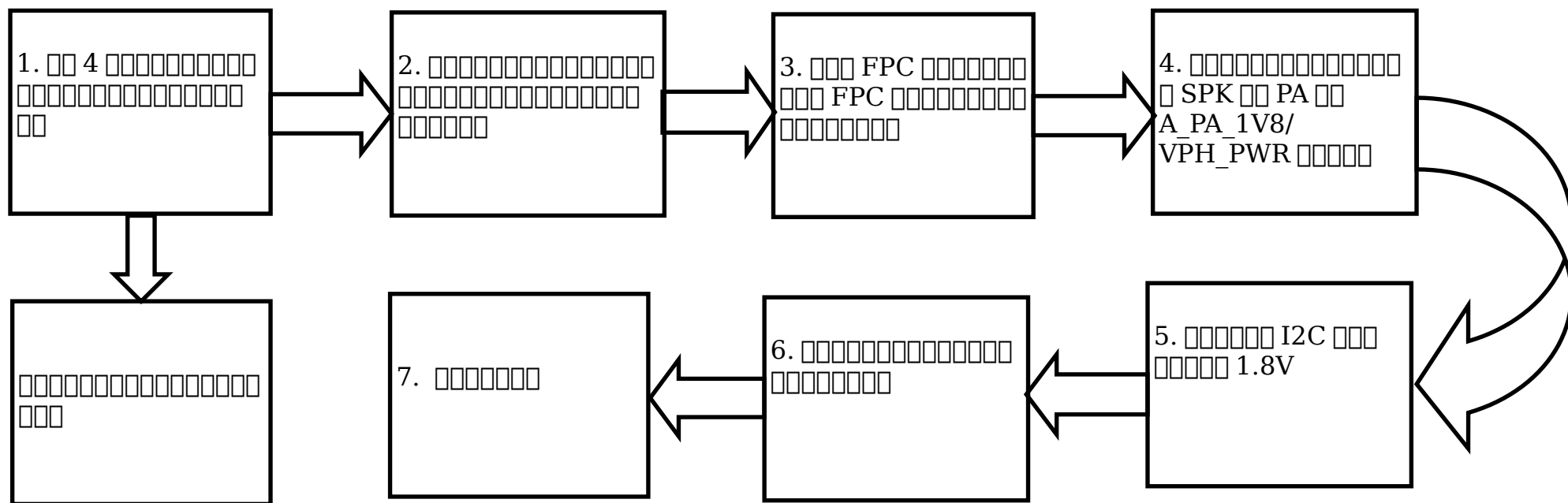


Audio SPK □□



□□□□□□□□

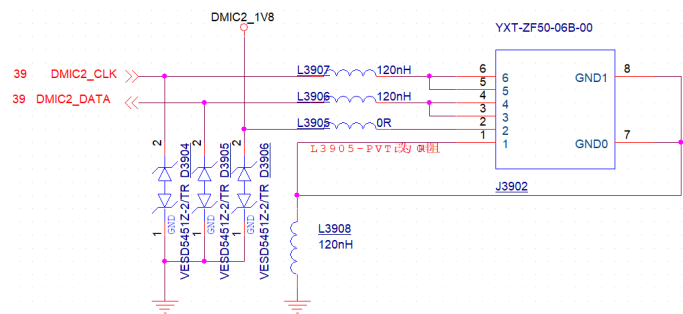
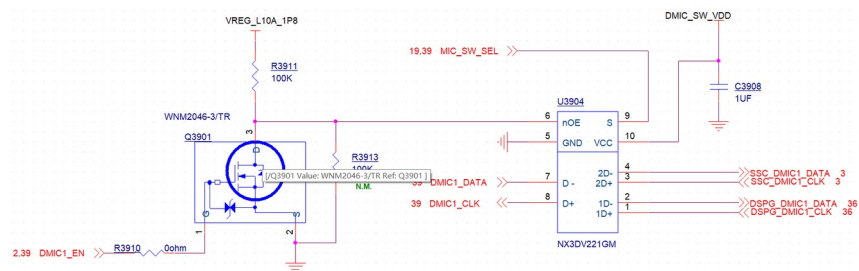
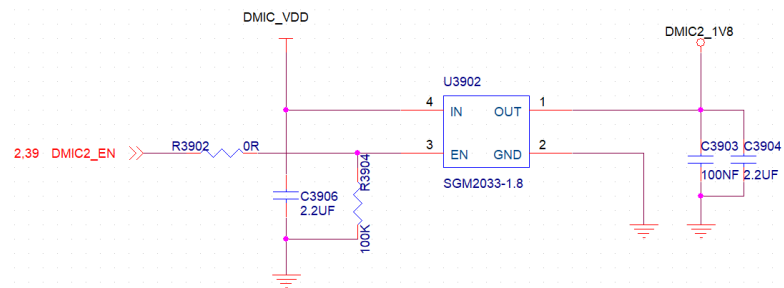
Steps



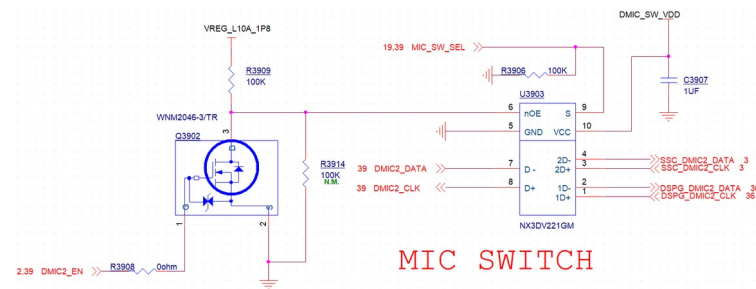
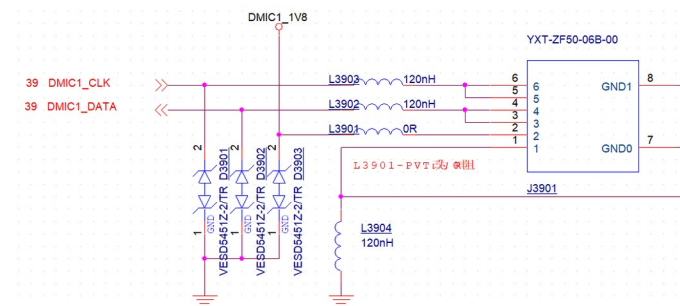
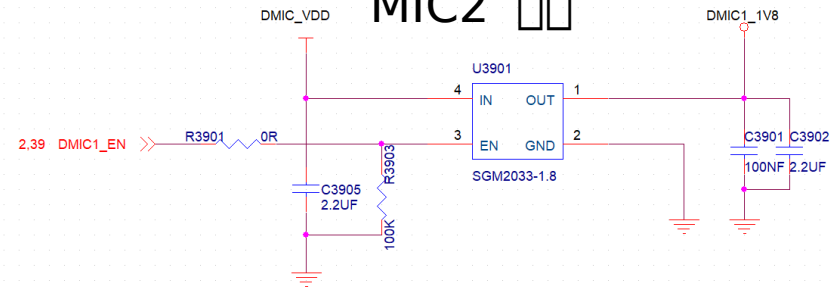
□□□□□□□□

□□□□□□□□ :

MIC1 □□

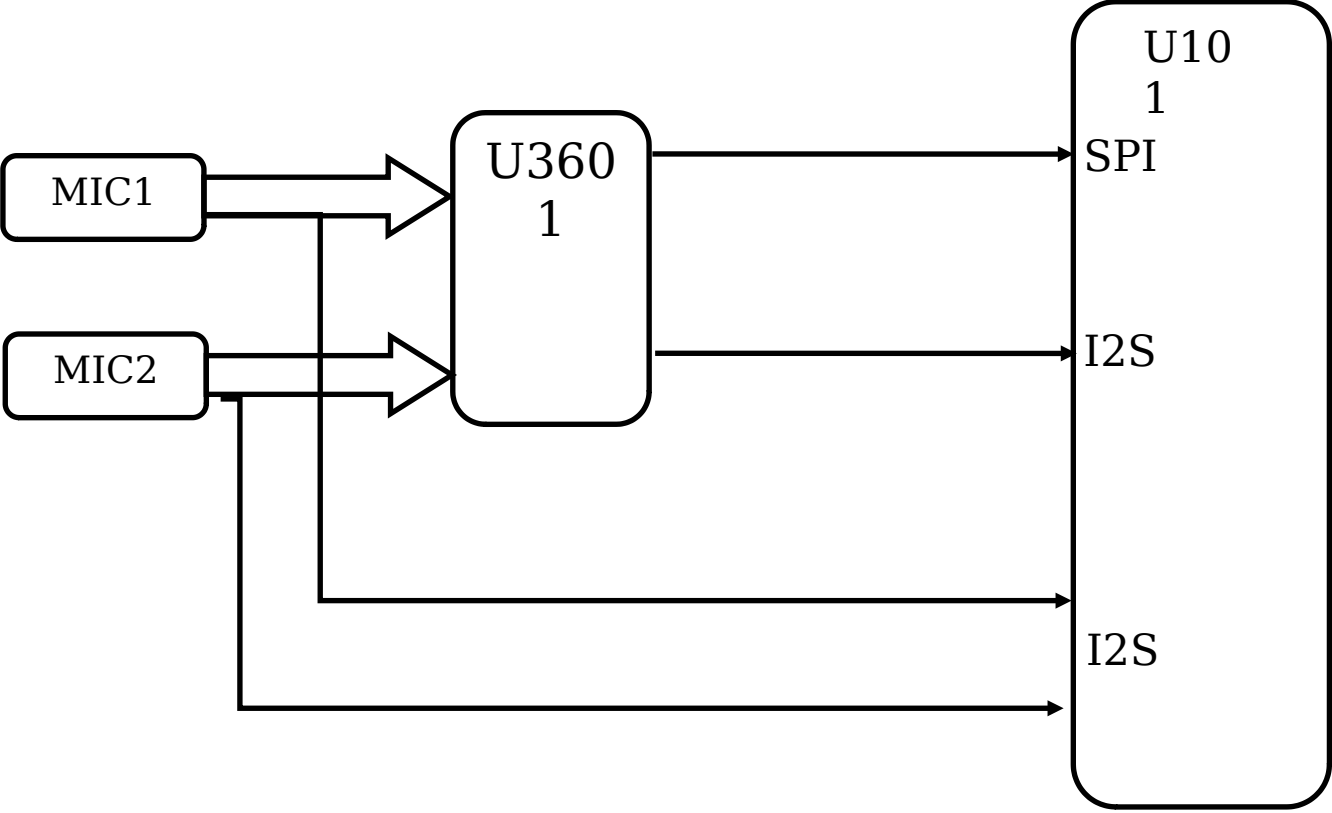


MIC2 □□



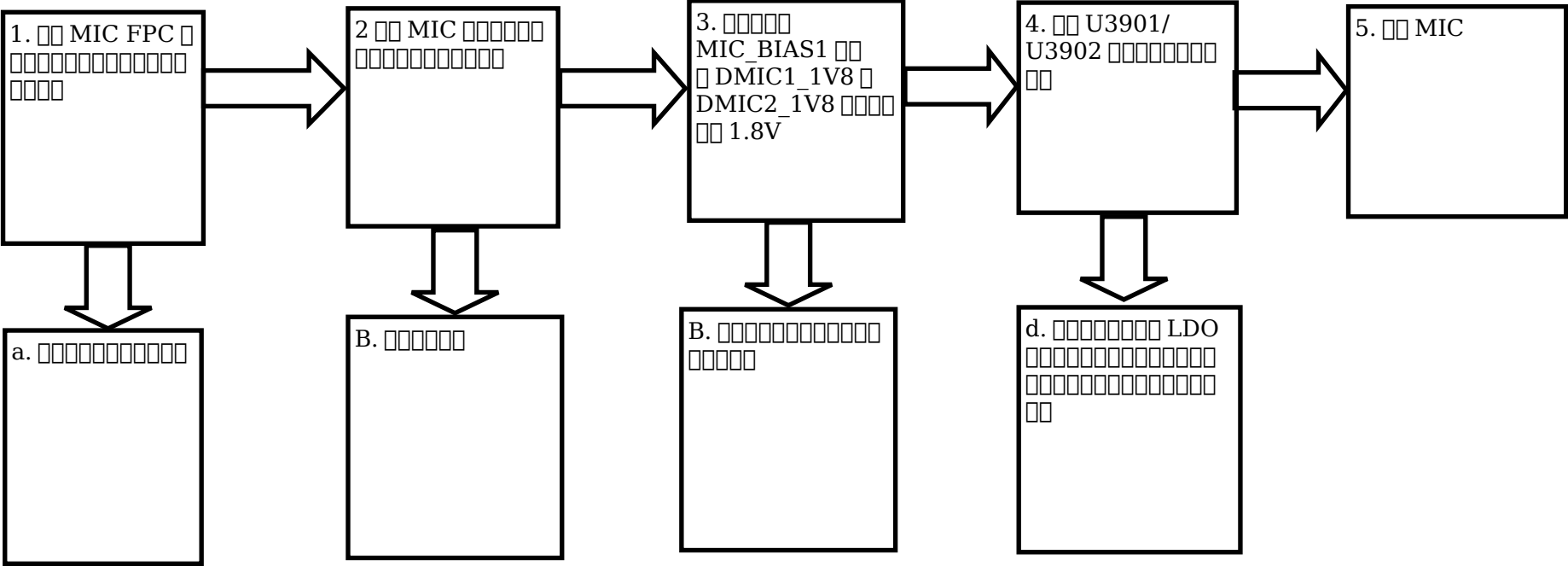
MIC SWITCH

MIC 接口



□□□□□□□□

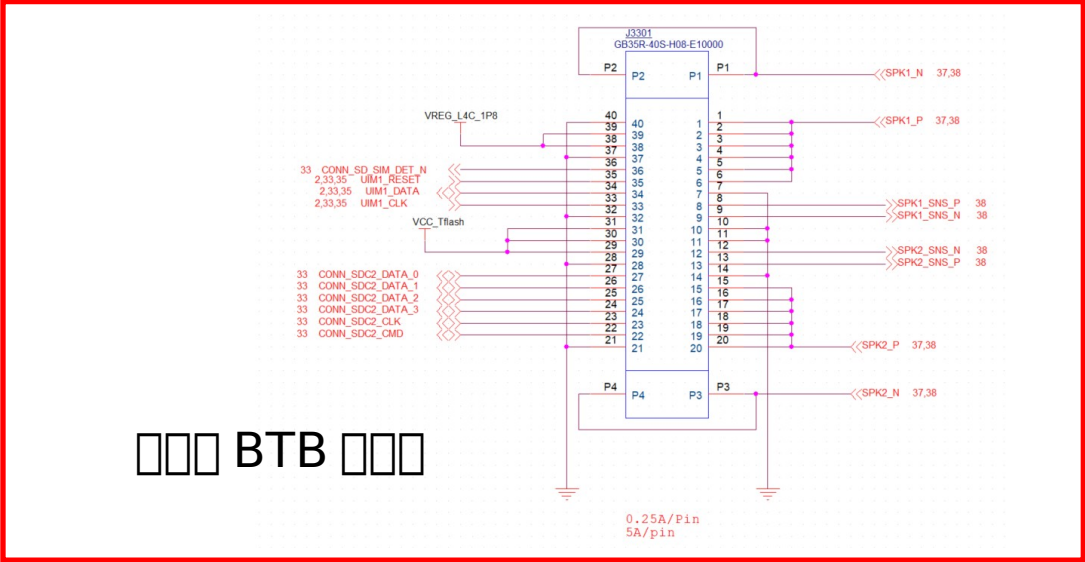
Steps



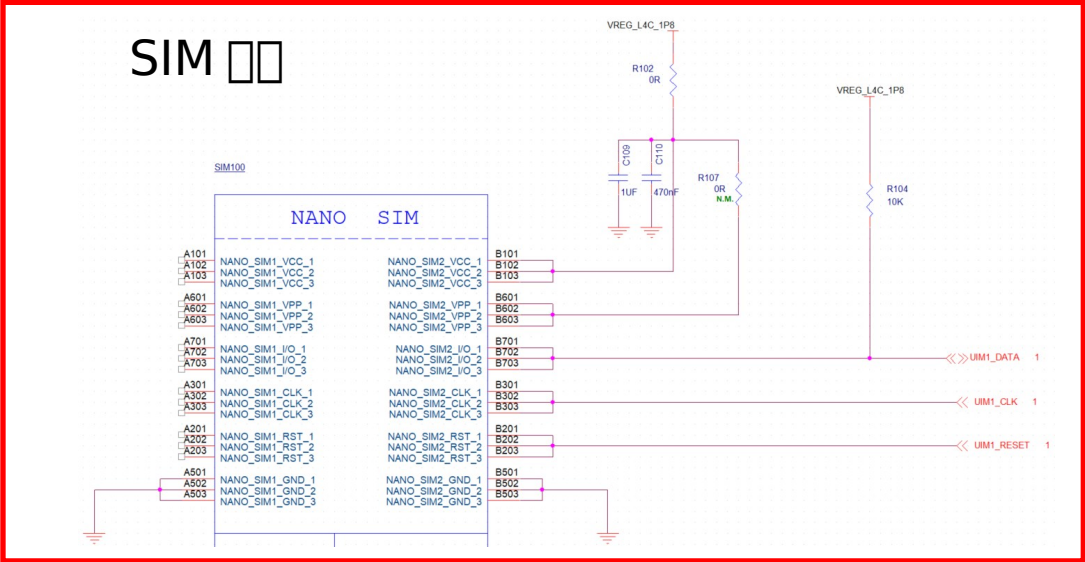
10. SIM □□□□□□

SIM

SIM :

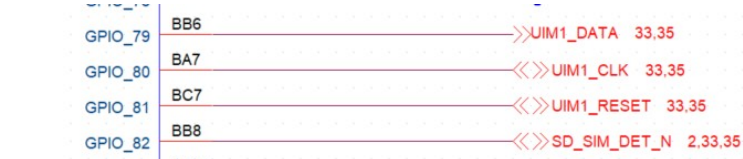


BTB

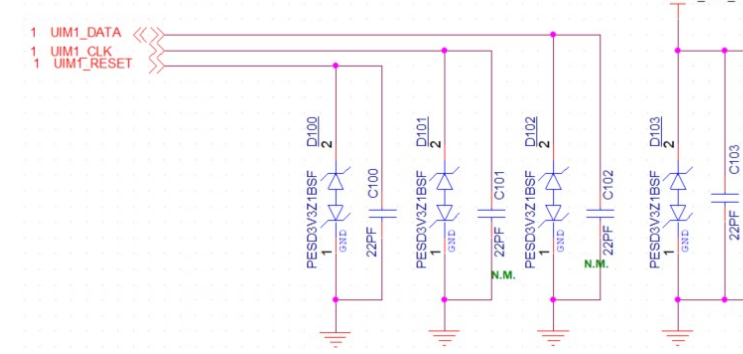


SIM

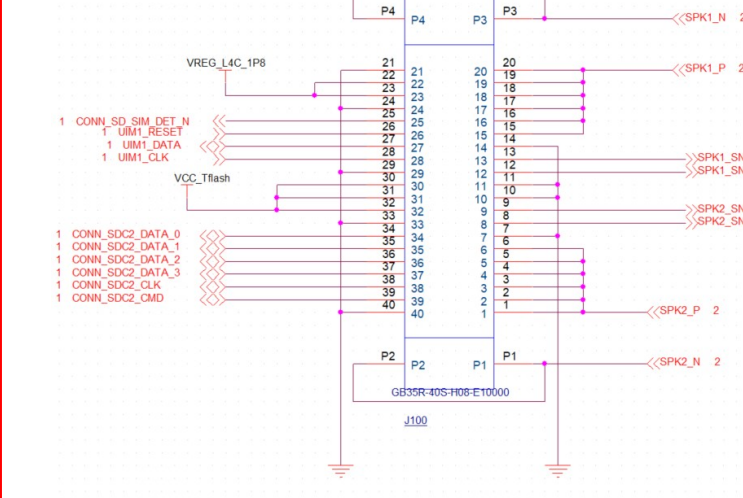
SM-7150



TVS

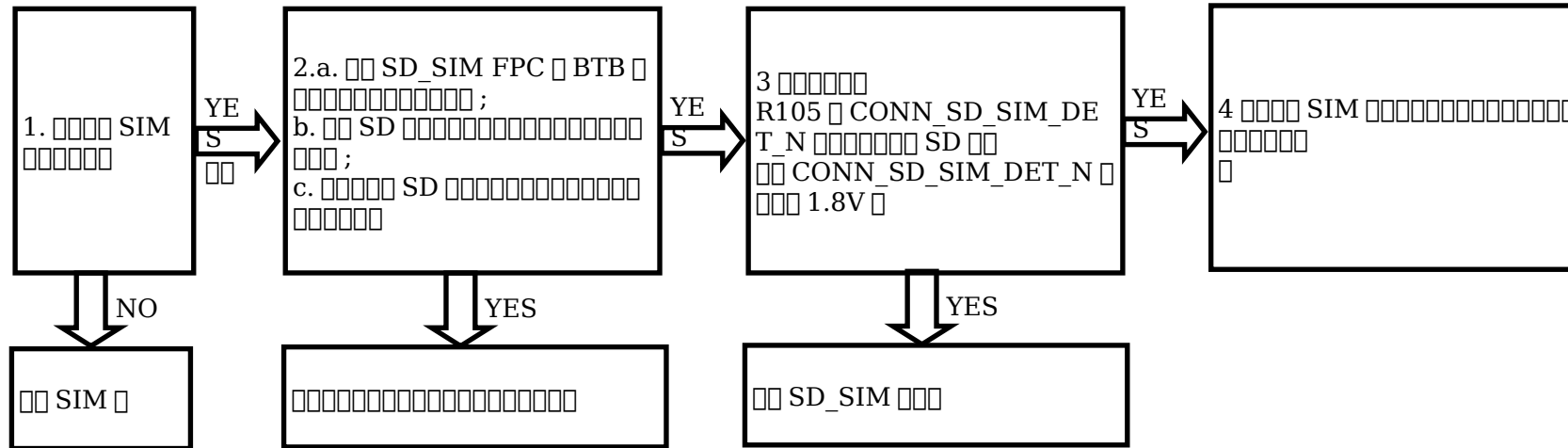


BTB

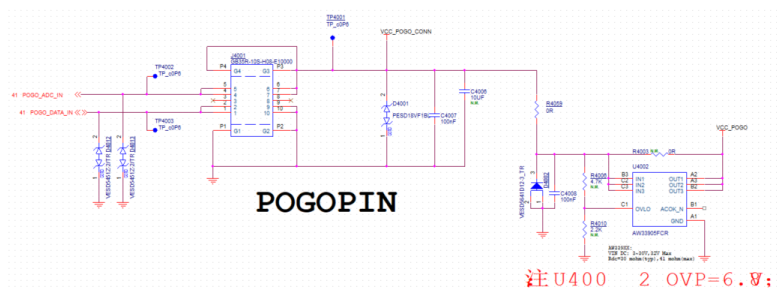
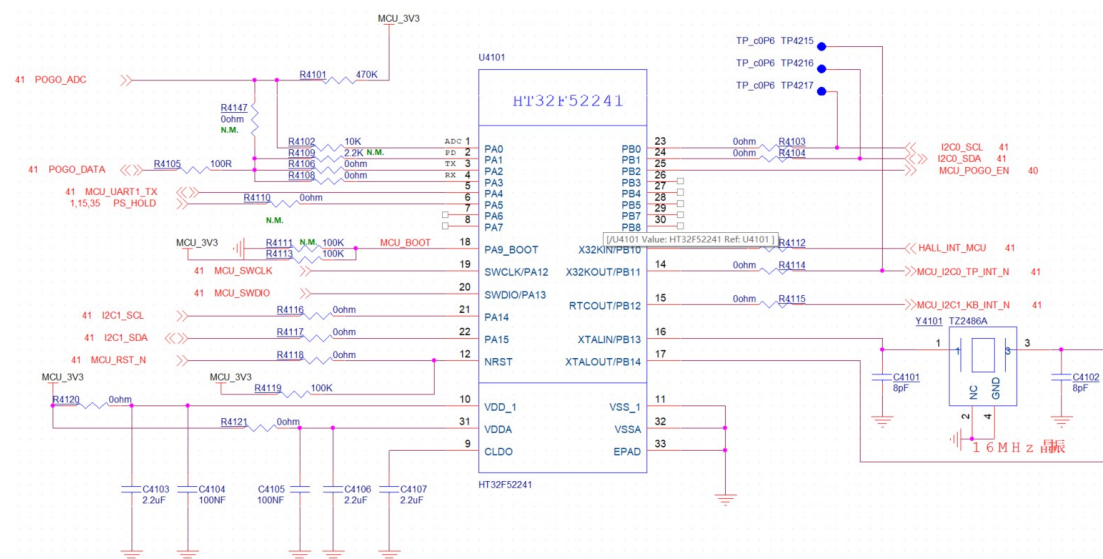


SIM 检查流程图

Steps

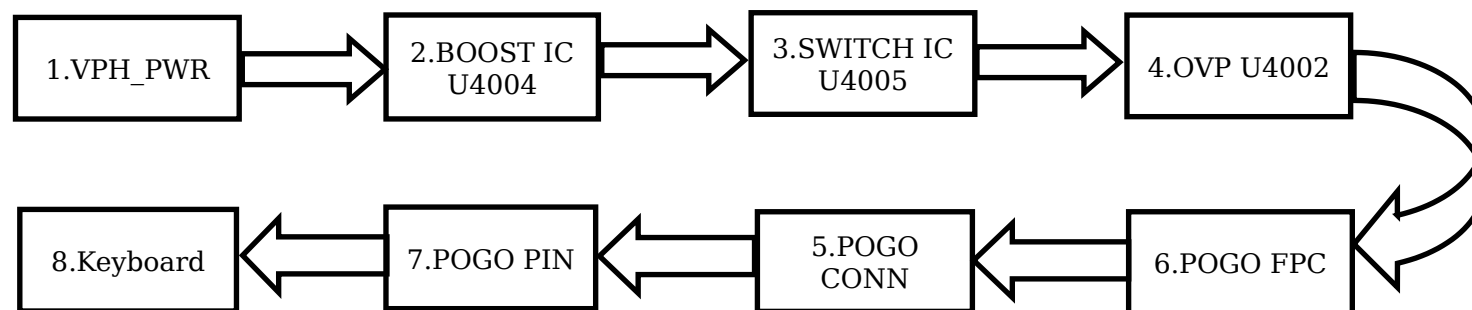


10. □□□□□□□

[illegible]



Key board Power Path



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Steps

